

Table with 4 columns: No., Information, Data Class, Type. Contains technical specifications for various components.

СРО ИСЭ

Table with 4 columns: No., Information, Data Class, Type. Contains technical specifications for various components.

СРО ИСЭ

286

Table with 4 columns: No., Information, Data Class, Type. Contains technical specifications for various components.

Grid Coupling Transformer

Table with 4 columns: No., Information, Data Class, Type. Contains technical specifications for various components.

Protection and Automation Functions

Table with 4 columns: No., Information, Data Class, Type. Contains technical specifications for various components.

СРО ИСЭ

Table with 4 columns: No., Information, Data Class, Type. Contains technical specifications for various components.

Inherent Circuit-Breaker Failure Protection

The Circuit-Breaker Failure Protection function (CBFP) stops the supply of the power substations (PS) and prevents a main circuit breaker (MCB) from re-closing.

Structure of the Function

The function checks for circuit breaker failure (CBF) and initiates the necessary actions to prevent the circuit breaker from re-closing.

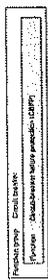


Figure 2-1. Structure of the function.

The function is implemented by the protection and automation system (PAS) in the control room of the substation.

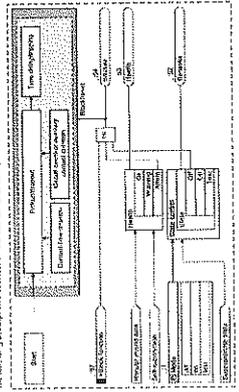


Figure 2-1. Structure of the function.

СРО ИСЭ

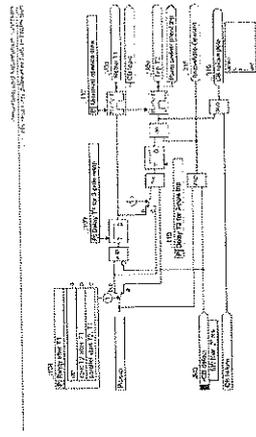


Figure 7-5 Power Output of the Class-B Driver Output Protection Function

7.1.4 Application and Setting Notes

Figure 7-5 shows the typical circuit of the Class-B Driver Output Protection Function. In the case of the Class-B Driver Output Protection Function, the MOSFET is driven by the Class-B Driver Output Protection Function. The Class-B Driver Output Protection Function is designed to protect the MOSFET from over-current and over-temperature during the switching of the load. The Class-B Driver Output Protection Function is designed to protect the MOSFET from over-current and over-temperature during the switching of the load.

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 CLASS-B DRIVER OUTPUT PROTECTION FUNCTION
 CONFIDENTIAL

7.1.5 Current Feedback Protection with External Diode, Thermal Regulation and 3-Wire Voltage

Configuration of Internal Sampling Source (Internal Possibility Function)

The internal sampling source (Internal Possibility Function) is used to monitor the current of the MOSFET. The internal sampling source is used to monitor the current of the MOSFET. The internal sampling source is used to monitor the current of the MOSFET.

Parameter Setting Table

Parameter Name	Description
Parameter Value	Internal sampling source (Internal Possibility Function)

Parameter Setting Table

Parameter Name	Description
Parameter Value	Internal sampling source (Internal Possibility Function)

Parameter Setting Table

Parameter Name	Description
Parameter Value	Internal sampling source (Internal Possibility Function)

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Parameter Value	Internal sampling source (Internal Possibility Function)

Parameter Setting Table

Parameter Name	Description
Parameter Value	Internal sampling source (Internal Possibility Function)

7.1.6 Thermal Regulation

Configuration of Thermal Regulation

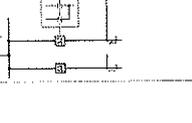


Figure 7-6 Configuration of Thermal Regulation

The thermal regulation function is used to monitor the temperature of the MOSFET. The thermal regulation function is used to monitor the temperature of the MOSFET. The thermal regulation function is used to monitor the temperature of the MOSFET.

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 THERMAL REGULATION
 CONFIDENTIAL

7.1.7 Current Feedback Protection with External Diode, Thermal Regulation and 3-Wire Voltage

Configuration of Internal Sampling Source (Internal Possibility Function)

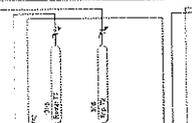


Figure 7-7 Current Feedback Protection with External Diode, Thermal Regulation and 3-Wire Voltage

The current feedback protection function is used to monitor the current of the MOSFET. The current feedback protection function is used to monitor the current of the MOSFET. The current feedback protection function is used to monitor the current of the MOSFET.

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 CURRENT FEEDBACK PROTECTION
 CONFIDENTIAL

7.1.8 Input Signal

Configuration of Input Signal



Figure 7-8 Input Signal

The input signal function is used to monitor the input signal of the MOSFET. The input signal function is used to monitor the input signal of the MOSFET. The input signal function is used to monitor the input signal of the MOSFET.

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 INPUT SIGNAL
 CONFIDENTIAL

7.1.9 Power Output

Configuration of Power Output

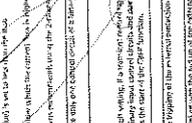


Figure 7-9 Power Output

The power output function is used to monitor the power output of the MOSFET. The power output function is used to monitor the power output of the MOSFET. The power output function is used to monitor the power output of the MOSFET.

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 POWER OUTPUT
 CONFIDENTIAL

7.1.10 Parameter Setting

Configuration of Parameter Setting

Parameter Name	Description
Parameter Value	Parameter Setting

Figure 7-10 Parameter Setting

The parameter setting function is used to monitor the parameter setting of the MOSFET. The parameter setting function is used to monitor the parameter setting of the MOSFET. The parameter setting function is used to monitor the parameter setting of the MOSFET.

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 PARAMETER SETTING
 CONFIDENTIAL

7.1.11 Parameter Setting

Configuration of Parameter Setting

Parameter Name	Description
Parameter Value	Parameter Setting

The parameter setting function is used to monitor the parameter setting of the MOSFET. The parameter setting function is used to monitor the parameter setting of the MOSFET. The parameter setting function is used to monitor the parameter setting of the MOSFET.

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 PARAMETER SETTING
 CONFIDENTIAL

7.1.12 Parameter Setting

Configuration of Parameter Setting

Parameter Name	Description
Parameter Value	Parameter Setting

The parameter setting function is used to monitor the parameter setting of the MOSFET. The parameter setting function is used to monitor the parameter setting of the MOSFET. The parameter setting function is used to monitor the parameter setting of the MOSFET.

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 PARAMETER SETTING
 CONFIDENTIAL

288

2.2.4.13 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.

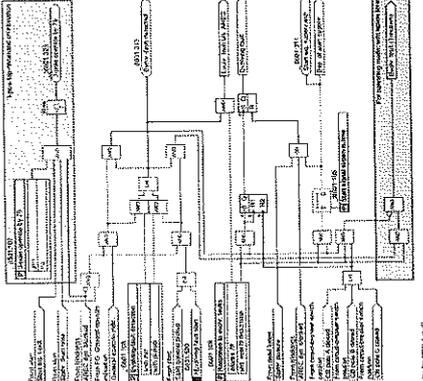


Figure 2-23. Quick Access, Reliability Function Block for Standby Fault Detection

2.2.4.14 **Timing Indicators and Class Control**

Timing indicators are used to monitor the timing of signals within a system. They are typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the timing logic. The software typically consists of a set of algorithms that manage the hardware resources.

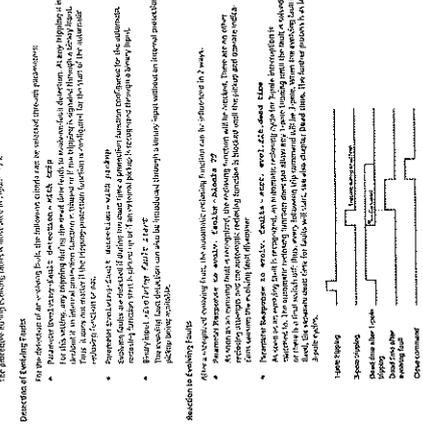


Figure 2-24. Timing Indicators and Class Control Function Block

2.2.4.15 **Cycle Timing Function Logic for the Fly of the Final Driver Reliability Detector**

The cycle timing function logic is used to monitor the timing of signals within a system. It is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the timing logic. The software typically consists of a set of algorithms that manage the hardware resources.

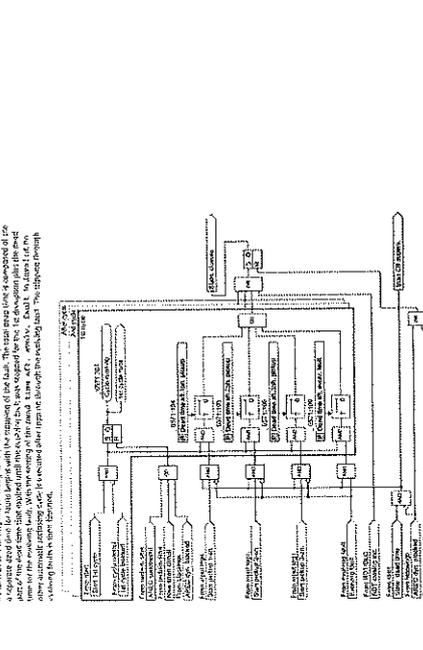


Figure 2-25. Cycle Timing Function Logic for the Fly of the Final Driver Reliability Detector

2.2.4.16 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.



Figure 2-26. Controlled Access Functionality Function Block

2.2.4.17 **Quick Access, Reliability Function Block for Standby Fault Detection**

Quick access, reliability function block for standby fault detection is used to monitor the timing of signals within a system. It is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the timing logic. The software typically consists of a set of algorithms that manage the hardware resources.

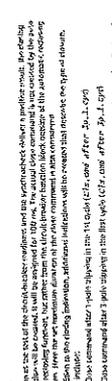


Figure 2-27. Quick Access, Reliability Function Block for Standby Fault Detection

2.2.4.18 **Cycle Timing Function Logic for the Fly of the Final Driver Reliability Detector**

The cycle timing function logic is used to monitor the timing of signals within a system. It is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the timing logic. The software typically consists of a set of algorithms that manage the hardware resources.



Figure 2-28. Cycle Timing Function Logic for the Fly of the Final Driver Reliability Detector

2.2.4.19 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.

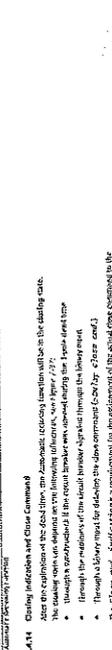


Figure 2-29. Controlled Access Functionality Function Block

2.2.4.20 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.



Figure 2-30. Controlled Access Functionality Function Block

2.2.4.21 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.

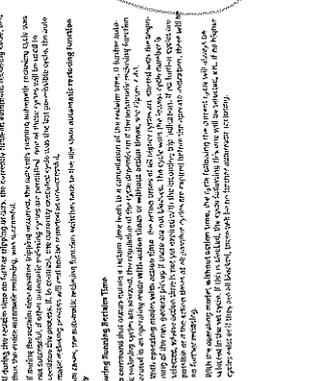


Figure 2-31. Controlled Access Functionality Function Block

2.2.4.22 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.

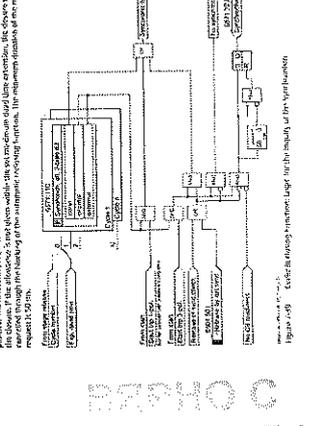


Figure 2-32. Controlled Access Functionality Function Block

2.2.4.23 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.

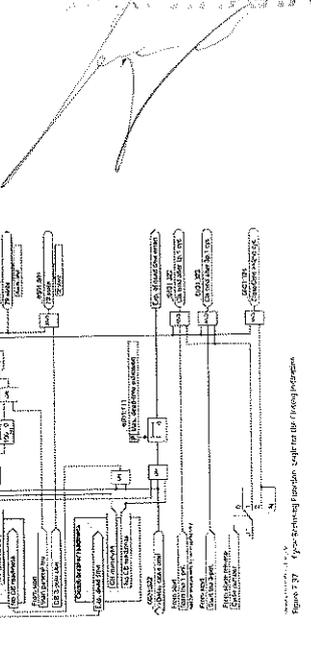


Figure 2-33. Controlled Access Functionality Function Block

2.2.4.24 **Controlled Access Functionality**

Controlled access is the ability to restrict access to a system or component based on a set of rules. This is typically implemented using a combination of hardware and software. The hardware typically consists of a set of logic gates and flip-flops that implement the access rules. The software typically consists of a set of algorithms that manage the hardware resources.



Figure 2-34. Controlled Access Functionality Function Block

7.3.1 Overview of Functions

- The External Tip Initiation 3-Pole
- Approves the 3-pole external tip initiation for the 3-pole system
- Enables the 3-pole external tip initiation for the 3-pole system
- Enables the 3-pole external tip initiation for the 3-pole system
- Enables the 3-pole external tip initiation for the 3-pole system

7.3.2 Structure of the Function

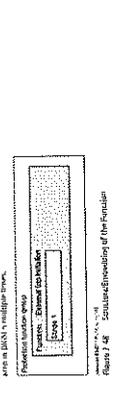


Figure 7.48: Structure of the Function

Information List

Item No.	Information	Item No.	Type
1
2
3
4
5
6
7
8
9
10

Figure 7.49: Information List

Stage Description

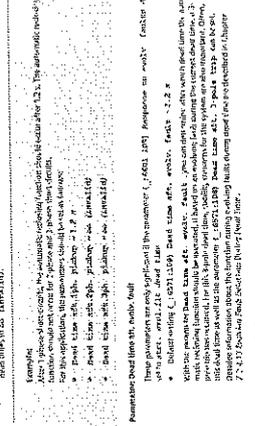


Figure 7.50: Stage Description

Application and Setting Notes

Parameter	Description	Setting
...
...
...
...

Figure 7.51: Application and Setting Notes

7.4.1 Overview of Functions

- The Overview Protection, Phases (OPP)
- Enables the 3-pole external tip initiation for the 3-pole system
- Enables the 3-pole external tip initiation for the 3-pole system

7.4.2 Structure of the Function

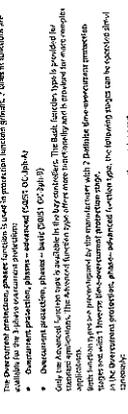


Figure 7.52: Structure of the Function

Information List

Item No.	Information	Item No.	Type
1
2
3
4
5
6
7
8
9
10

Figure 7.53: Information List

Stage Description

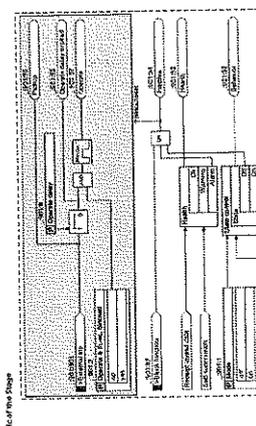


Figure 7.54: Stage Description

Application and Setting Notes

Parameter	Description	Setting
...
...
...
...

Figure 7.55: Application and Setting Notes

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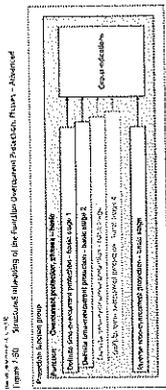
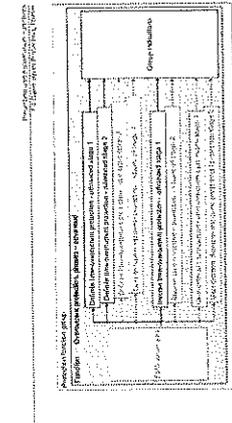


Figure 7.51: Description of the Function Development Protection - Stage - lock

If the anti-internal function is blocked in the following an internal lock function can influence the protection stage and the filter block.

- Automatic blocking (AB) is active.
- Control signal is active.
- Filtering input signal.
- If the function is equipped with an in-advance detection function, the stage can be enabled again by a signal from the in-advance detection function (control signal).

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41

7.4.3 Filter for RMS Value Gain

7.4.3.1 Description

The stage receives filter data in order to calculate the RMS value of the signal. The calculation is performed in the digital domain. The RMS value is calculated by the square root of the mean value of the squared signal. The RMS value is calculated by the square root of the mean value of the squared signal. The RMS value is calculated by the square root of the mean value of the squared signal.

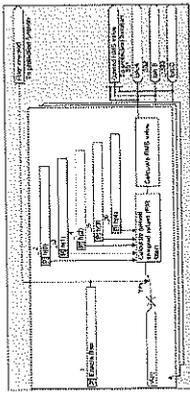


Figure 7.52: Logic diagram of the Function Block Filter

The filter block calculates the RMS value of the signal. The RMS value is calculated by the square root of the mean value of the squared signal. The RMS value is calculated by the square root of the mean value of the squared signal. The RMS value is calculated by the square root of the mean value of the squared signal.

NOTE

A filter qualification test is performed as an auxiliary function. The test results are stored in the filter block. The test results are stored in the filter block. The test results are stored in the filter block. The test results are stored in the filter block.

Functional Measured Values

Value	Description	Priority	Category	Severity	Reference ID
RMS	RMS value of the input signal	1	A	High	100000
AB	Automatic blocking (AB) is active	2	A	High	100000
CS	Control signal is active	3	A	High	100000

41

7.4.3.4 Stage with Definite-Time Characteristic Curve

7.4.3.4.1 Description

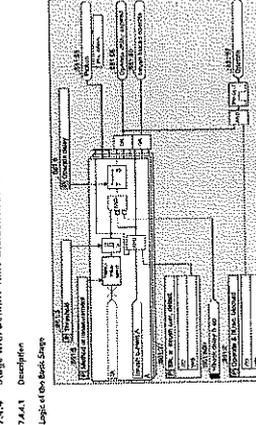


Figure 7.53: Logic Diagram of the Definite-Time Development Protection - Filter

ВЕРНО С
ОПРЕДЕЛЕНИЕ

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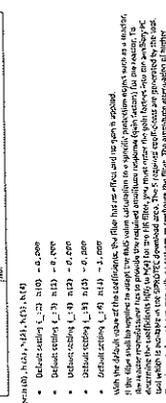
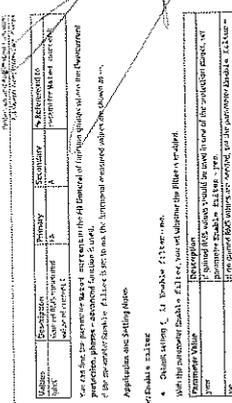


Figure 7.51: Description of the Function Development Protection - Stage - lock

If the anti-internal function is blocked in the following an internal lock function can influence the protection stage and the filter block.

- Automatic blocking (AB) is active.
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Functional Measured Values

Value	Description	Priority	Category	Severity	Reference ID
RMS	RMS value of the input signal	1	A	High	100000
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CS	Control signal is active	3	A	High	100000

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7.4.3.4 Stage with Definite-Time Characteristic Curve

7.4.3.4.1 Description

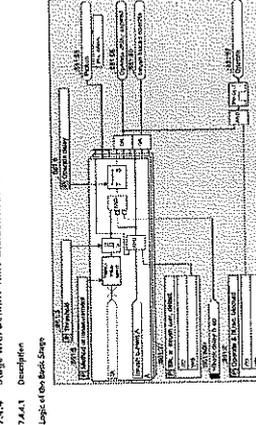


Figure 7.53: Logic Diagram of the Definite-Time Development Protection - Filter

ВЕРНО С
ОПРЕДЕЛЕНИЕ

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Figure 7.51: Description of the Function Development Protection - Stage - lock

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Functional Measured Values

Value	Description	Priority	Category	Severity	Reference ID
RMS	RMS value of the input signal	1	A	High	100000
AB	Automatic blocking (AB) is active	2	A	High	100000
CS	Control signal is active	3	A	High	100000

41

7.4.3.4 Stage with Definite-Time Characteristic Curve

7.4.3.4.1 Description

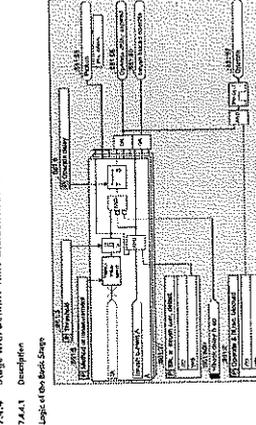


Figure 7.53: Logic Diagram of the Definite-Time Development Protection - Filter

ВЕРНО С
ОПРЕДЕЛЕНИЕ

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Figure 7.51: Description of the Function Development Protection - Stage - lock

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AB	Automatic blocking (AB) is active	2	A	High	100000
CS	Control signal is active	3	A	High	100000

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7.4.3.4 Stage with Definite-Time Characteristic Curve

7.4.3.4.1 Description

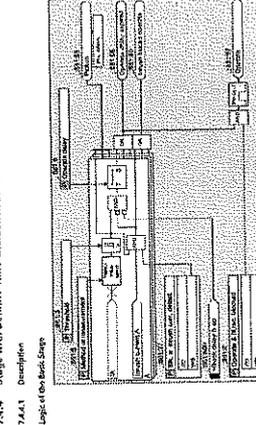


Figure 7.53: Logic Diagram of the Definite-Time Development Protection - Filter

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ОПРЕДЕЛЕНИЕ

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Figure 7.51: Description of the Function Development Protection - Stage - lock

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AB	Automatic blocking (AB) is active	2	A	High	100000
CS	Control signal is active	3	A	High	100000

41

74.2.2.2. Application of Settings

- ... 74.2.2.2. Application of Settings

Table with 3 columns: Parameter, Description, Value

Parameter Description Value
74.2.2.2. Application of Settings
74.2.2.2. Application of Settings

74.2.2.2. Application of Settings

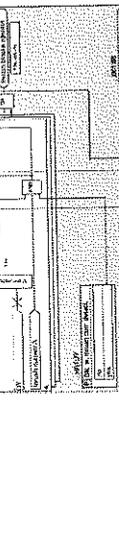
74.2.2.2. Application of Settings

74.2.2.2. Application of Settings

Table with 4 columns: Parameter, Description, Value, Units

74.2.2.2. Application of Settings

74.2.2.2. Application of Settings



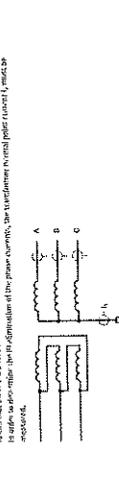
601.020.100 (Advanced)

- ... 74.2.2.2. Application of Settings

Table with 4 columns: Parameter, Description, Value, Units

74.2.2.2. Application of Settings

74.2.2.2. Application of Settings



601.020.100 (Advanced)

- ... 74.2.2.2. Application of Settings

Item No.	Description	Quantity	Unit	Estimated Cost	Remarks
241.1	1.00000000	1.00	sq. ft.	1.00	...
241.2
241.3
241.4
241.5

Information List

Item No.	Description	Quantity	Unit	Estimated Cost	Remarks
241.1
241.2
241.3

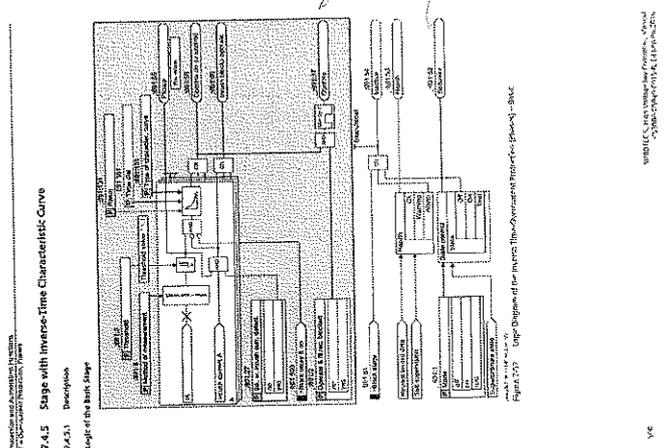


Figure 7-45 Logic Diagram of the Inverse-Time Characteristic Curve

Item No.	Description	Quantity	Unit	Estimated Cost	Remarks
241.1
241.2
241.3
241.4
241.5

Information List

Item No.	Description	Quantity	Unit	Estimated Cost	Remarks
241.1
241.2
241.3

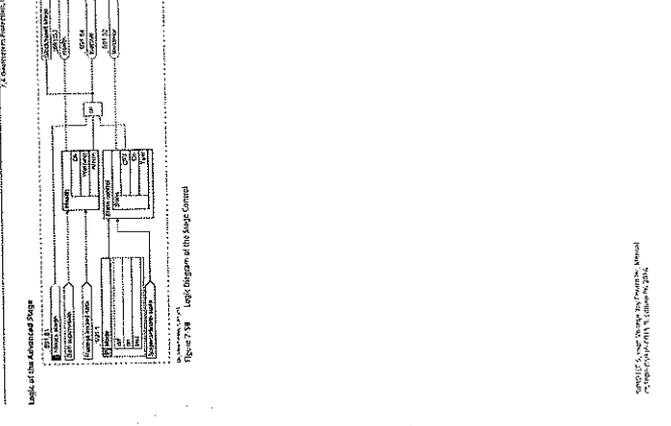


Figure 7-46 Logic Diagram of the Stage Control

Item No.	Description	Quantity	Unit	Estimated Cost	Remarks
241.1
241.2
241.3
241.4
241.5

Information List

Item No.	Description	Quantity	Unit	Estimated Cost	Remarks
241.1
241.2
241.3

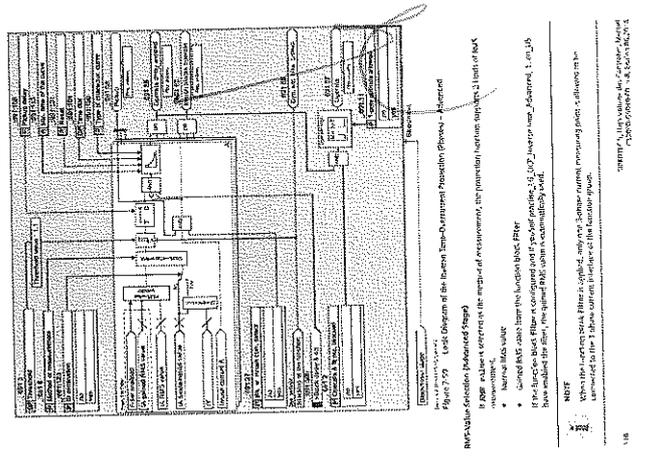
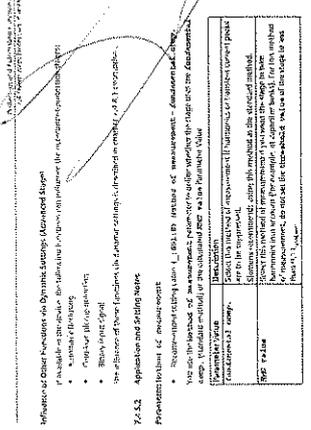


Figure 7-47 Logic Diagram of the Burn-In Circuit Protection (Prevent) - Element



Induction of EMF in Transformer
 The primary winding of a transformer is connected to an AC source. The secondary winding is connected to a load. The magnetic flux in the core is the same for both windings. The induced EMF in the secondary winding is proportional to the number of turns in the secondary winding.

Transformer Equations

$$\frac{V_p}{V_s} = \frac{N_p}{N_s}$$

$$\frac{I_p}{I_s} = \frac{N_s}{N_p}$$

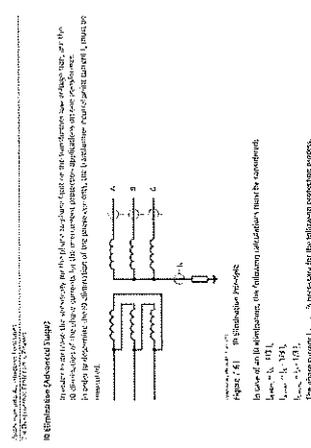
Parameter Table
 Table with 4 columns: Parameter, Value, Unit, and Remarks. Parameters include: Transformer ratio, Inductance, etc.

Parameter	Value	Unit	Remarks
Transformer ratio	1.0		
Inductance	1.0	H	
...

Notes
 1. The transformer ratio is 1.0.
 2. The inductance is 1.0 H.

Parameter	Value	Unit	Remarks
Transformer ratio	1.0		
Inductance	1.0	H	
...

Notes
 1. The transformer ratio is 1.0.
 2. The inductance is 1.0 H.



Induction of EMF in Transformer
 The primary winding of a transformer is connected to an AC source. The secondary winding is connected to a load. The magnetic flux in the core is the same for both windings. The induced EMF in the secondary winding is proportional to the number of turns in the secondary winding.

Transformer Equations

$$\frac{V_p}{V_s} = \frac{N_p}{N_s}$$

$$\frac{I_p}{I_s} = \frac{N_s}{N_p}$$

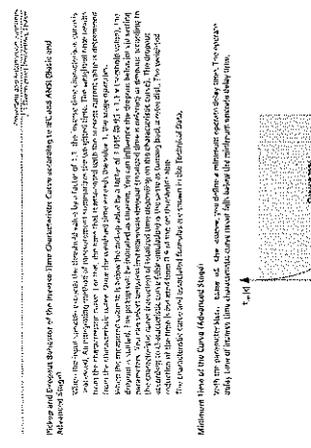
Parameter Table
 Table with 4 columns: Parameter, Value, Unit, and Remarks. Parameters include: Transformer ratio, Inductance, etc.

Parameter	Value	Unit	Remarks
Transformer ratio	1.0		
Inductance	1.0	H	
...

Notes
 1. The transformer ratio is 1.0.
 2. The inductance is 1.0 H.

Parameter	Value	Unit	Remarks
Transformer ratio	1.0		
Inductance	1.0	H	
...

Notes
 1. The transformer ratio is 1.0.
 2. The inductance is 1.0 H.



Measurement of the Diode (Advanced Stage)
 The diode is connected in a circuit with a current source and a voltmeter. The current is varied, and the voltage across the diode is measured. The resulting curve shows the characteristic of the diode.

Parameter Table
 Table with 4 columns: Parameter, Value, Unit, and Remarks. Parameters include: Current, Voltage, etc.

Notes
 1. The current is varied from 0 to 10 mA.
 2. The voltage across the diode is measured.

Parameter	Value	Unit	Remarks
Current	0.0	mA	
Voltage	0.0	V	
...

Notes
 1. The current is varied from 0 to 10 mA.
 2. The voltage across the diode is measured.

Parameter	Value	Unit	Remarks
Current	0.0	mA	
Voltage	0.0	V	
...

Notes
 1. The current is varied from 0 to 10 mA.
 2. The voltage across the diode is measured.



Induction of EMF in Transformer
 The primary winding of a transformer is connected to an AC source. The secondary winding is connected to a load. The magnetic flux in the core is the same for both windings. The induced EMF in the secondary winding is proportional to the number of turns in the secondary winding.

Transformer Equations

$$\frac{V_p}{V_s} = \frac{N_p}{N_s}$$

$$\frac{I_p}{I_s} = \frac{N_s}{N_p}$$

Parameter Table
 Table with 4 columns: Parameter, Value, Unit, and Remarks. Parameters include: Transformer ratio, Inductance, etc.

Parameter	Value	Unit	Remarks
Transformer ratio	1.0		
Inductance	1.0	H	
...

Notes
 1. The transformer ratio is 1.0.
 2. The inductance is 1.0 H.

Parameter	Value	Unit	Remarks
Transformer ratio	1.0		
Inductance	1.0	H	
...

Notes
 1. The transformer ratio is 1.0.
 2. The inductance is 1.0 H.

7.5.5 Stages with Inverse-Time Characteristic Curve

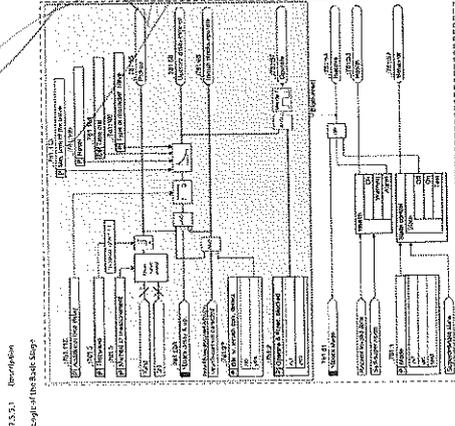


Figure 7.17 Logic Diagram of the Inverse-Time Characteristic Protection (Control) Block

7.5.3 Stages with Inverse-Time Characteristic Curve

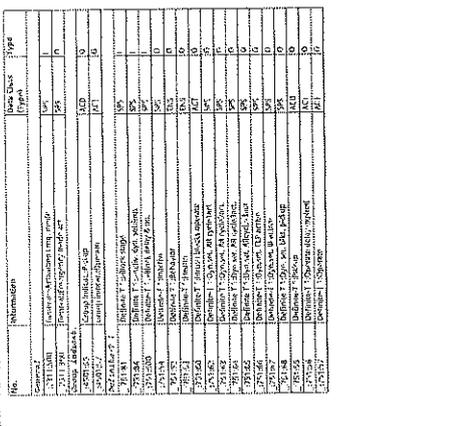


Figure 7.18 Logic Diagram of the Inverse-Time Characteristic Protection (Control) Block

No.	Alarms	Time Base (min)	Type
1	Emergency	5	NO
2	Emergency	5	NO
3	Emergency	5	NO
4	Emergency	5	NO
5	Emergency	5	NO
6	Emergency	5	NO
7	Emergency	5	NO
8	Emergency	5	NO
9	Emergency	5	NO
10	Emergency	5	NO
11	Emergency	5	NO
12	Emergency	5	NO
13	Emergency	5	NO
14	Emergency	5	NO
15	Emergency	5	NO
16	Emergency	5	NO
17	Emergency	5	NO
18	Emergency	5	NO
19	Emergency	5	NO
20	Emergency	5	NO
21	Emergency	5	NO
22	Emergency	5	NO
23	Emergency	5	NO
24	Emergency	5	NO
25	Emergency	5	NO
26	Emergency	5	NO
27	Emergency	5	NO
28	Emergency	5	NO
29	Emergency	5	NO
30	Emergency	5	NO
31	Emergency	5	NO
32	Emergency	5	NO
33	Emergency	5	NO
34	Emergency	5	NO
35	Emergency	5	NO
36	Emergency	5	NO
37	Emergency	5	NO
38	Emergency	5	NO
39	Emergency	5	NO
40	Emergency	5	NO
41	Emergency	5	NO
42	Emergency	5	NO
43	Emergency	5	NO
44	Emergency	5	NO
45	Emergency	5	NO
46	Emergency	5	NO
47	Emergency	5	NO
48	Emergency	5	NO
49	Emergency	5	NO
50	Emergency	5	NO

Figure 7.19 Logic Diagram of the Inverse-Time Characteristic Protection (Control) Block

Minimum time of the curve (Advanced Stage)



Figure 7.20 Minimum Operating Time of the Curve

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.21 Blocking of the Stage (Blocked and Advanced Stage)

Method of Measurement (Blocked and Advanced Stage)

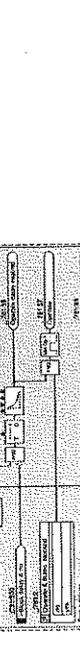


Figure 7.22 Method of Measurement (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.23 Blocking of the Stage (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.24 Blocking of the Stage (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.25 Blocking of the Stage (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)

No.	Alarms	Time Base (min)	Type
1	Emergency	5	NO
2	Emergency	5	NO
3	Emergency	5	NO
4	Emergency	5	NO
5	Emergency	5	NO
6	Emergency	5	NO
7	Emergency	5	NO
8	Emergency	5	NO
9	Emergency	5	NO
10	Emergency	5	NO
11	Emergency	5	NO
12	Emergency	5	NO
13	Emergency	5	NO
14	Emergency	5	NO
15	Emergency	5	NO
16	Emergency	5	NO
17	Emergency	5	NO
18	Emergency	5	NO
19	Emergency	5	NO
20	Emergency	5	NO
21	Emergency	5	NO
22	Emergency	5	NO
23	Emergency	5	NO
24	Emergency	5	NO
25	Emergency	5	NO
26	Emergency	5	NO
27	Emergency	5	NO
28	Emergency	5	NO
29	Emergency	5	NO
30	Emergency	5	NO
31	Emergency	5	NO
32	Emergency	5	NO
33	Emergency	5	NO
34	Emergency	5	NO
35	Emergency	5	NO
36	Emergency	5	NO
37	Emergency	5	NO
38	Emergency	5	NO
39	Emergency	5	NO
40	Emergency	5	NO
41	Emergency	5	NO
42	Emergency	5	NO
43	Emergency	5	NO
44	Emergency	5	NO
45	Emergency	5	NO
46	Emergency	5	NO
47	Emergency	5	NO
48	Emergency	5	NO
49	Emergency	5	NO
50	Emergency	5	NO

Figure 7.26 Minimum Operating Time of the Curve

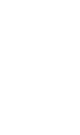


Figure 7.27 Minimum Operating Time of the Curve

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.28 Minimum Operating Time of the Curve

Method of Measurement (Blocked and Advanced Stage)



Figure 7.29 Method of Measurement (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.30 Blocking of the Stage (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.31 Blocking of the Stage (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.32 Blocking of the Stage (Blocked and Advanced Stage)

Blocking of the stage (Blocked and Advanced Stage)



Figure 7.33 Blocking of the Stage (Blocked and Advanced Stage)

Table with columns: No., Information, and Remarks. It lists various system components and their associated information units.

7.5.7 Backing of the Tripping by Device-Internal Impush-Current Detection

7.5.7.1

When the SUP or success cause... positive... when the... trip... is initiated... the... current... is detected...

Figure 7-21: Backing of the Tripping by Device-Internal Impush-Current Detection

Table with columns: No., Information, and Remarks. It lists various system components and their associated information units.

Figure 7-22: Backing of the Tripping by Device-Internal Impush-Current Detection

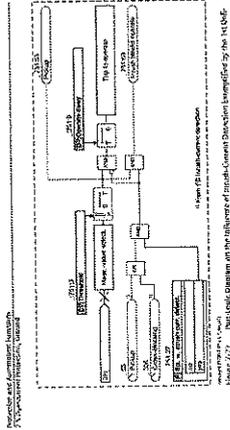


Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

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Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

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Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Figure 7-23: Principle of the Dynamic Settings in the Example of the Setting of the Dynamic Protection

Table with columns: No., Information, and Remarks. It lists various system components and their associated information units.

Figure 7-24: Application and Setting Data



Figure 7-25: Application and Setting Data

Table with columns: No., Information, and Remarks. It lists various system components and their associated information units.

Figure 7-26: Influence of Other Functions via Dynamic Settings

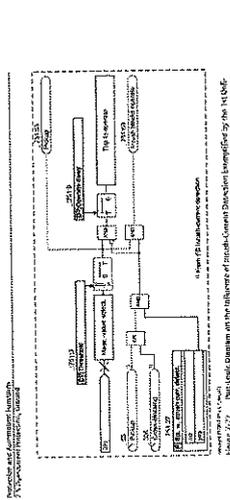


Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

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Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

Figure 7-27: Influence of Other Functions via Dynamic Settings

7.6 Directional Overcurrent Protection, Phases

7.6.1 Overview of Functions

- Directional discrimination (directional protection, phases (A+B+C))
- Tripping, then reclosing, of protective equipment
- Locking out a circuit or emergency equipment (phase) in order to allow the main protection to operate
- Forward blocking, i.e. blocking for faults based on directional protection (phase) at a distance
- Forward blocking (and protection) in each case with respect to both phase and time component of the fault

7.6.2 Structure of the Function

7.6.3 Stage Control

7.6.3.1 Description

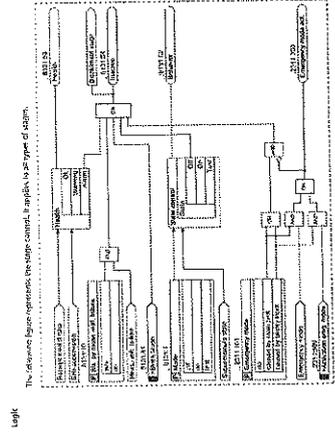


Figure 7.6.3 Logic Diagram of the Directional Overcurrent Protection, Phases (A+B+C)

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7.6.4 Stage with Definite-Time Characteristic Curve

7.6.4.1 Description

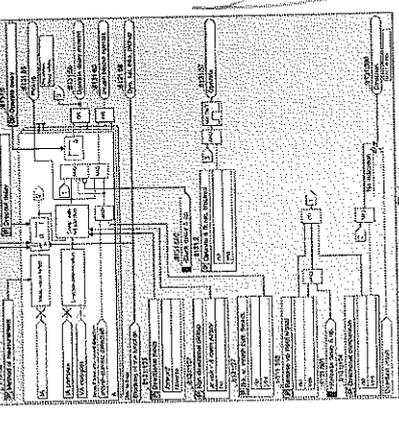


Figure 7.6.4 Logic Diagram of the Directional Overcurrent Protection, Phases (A+B+C)

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7.6.2 Application with Setting Meter (Advanced Stage)

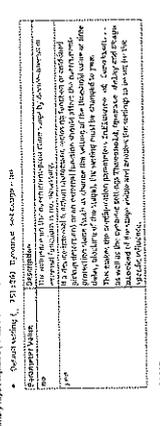


Figure 7.6.2 Logic Diagram of the Directional Overcurrent Protection, Phases (A+B+C)

Influence of AREC

The example of how the overcurrent stage (O) is applied in a test stage before automatic reclosing operation is presented in Figure 7.6.3. The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic. The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic. The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic.

Influence of External Events

The influence of external events on the configuration of the logic is presented in Figure 7.6.4. The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic. The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic.

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Logic of the Directional Protection (Advanced Stage)

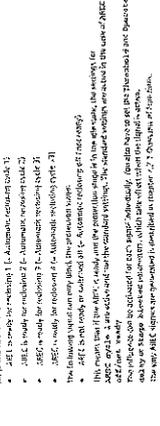


Figure 7.6.1 Logic Diagram of the Directional Overcurrent Protection, Phases (A+B+C)

Logic of the Backup Stage

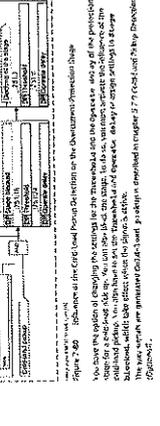


Figure 7.6.4 Logic Diagram of the Directional Overcurrent Protection, Phases (A+B+C)

The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic. The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic.

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Emergency Mode (Advanced Stage)



Figure 7.6.2 Logic Diagram of the Directional Overcurrent Protection, Phases (A+B+C)

Backup of the Stage with Missing Voltage Failure (Basic and Advanced Stage)



Figure 7.6.4 Logic Diagram of the Directional Overcurrent Protection, Phases (A+B+C)

The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic. The logic is based on the fact that the stage before an automatic reclosing function (AR) is a directional overcurrent stage (O) with a definite-time characteristic.

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Item #	Description	Unit	Quantity	Unit Price	Amount	Details of Work	
						Material	Labor
81122	Excavate 1.0 cubic yard	cu yd	1.00	1.50	1.50	Excavate 1.0 cubic yard (1.00 cu yd)	1.50
81123	Backfill 1.0 cubic yard	cu yd	1.00	1.50	1.50	Backfill 1.0 cubic yard (1.00 cu yd)	1.50
81124	Fill 1.0 cubic yard	cu yd	1.00	1.50	1.50	Fill 1.0 cubic yard (1.00 cu yd)	1.50
81125	Grade 1.0 cubic yard	cu yd	1.00	1.50	1.50	Grade 1.0 cubic yard (1.00 cu yd)	1.50
81126	Compaction 1.0 cubic yard	cu yd	1.00	1.50	1.50	Compaction 1.0 cubic yard (1.00 cu yd)	1.50
81127	Site Preparation 1.0 cubic yard	cu yd	1.00	1.50	1.50	Site Preparation 1.0 cubic yard (1.00 cu yd)	1.50
81128	Remove 1.0 cubic yard	cu yd	1.00	1.50	1.50	Remove 1.0 cubic yard (1.00 cu yd)	1.50
81129	Subgrade 1.0 cubic yard	cu yd	1.00	1.50	1.50	Subgrade 1.0 cubic yard (1.00 cu yd)	1.50
81130	Foundation 1.0 cubic yard	cu yd	1.00	1.50	1.50	Foundation 1.0 cubic yard (1.00 cu yd)	1.50
81131	Structure 1.0 cubic yard	cu yd	1.00	1.50	1.50	Structure 1.0 cubic yard (1.00 cu yd)	1.50
81132	Roofing 1.0 cubic yard	cu yd	1.00	1.50	1.50	Roofing 1.0 cubic yard (1.00 cu yd)	1.50
81133	Interior Finishes 1.0 cubic yard	cu yd	1.00	1.50	1.50	Interior Finishes 1.0 cubic yard (1.00 cu yd)	1.50
81134	Exterior Finishes 1.0 cubic yard	cu yd	1.00	1.50	1.50	Exterior Finishes 1.0 cubic yard (1.00 cu yd)	1.50
81135	Landscaping 1.0 cubic yard	cu yd	1.00	1.50	1.50	Landscaping 1.0 cubic yard (1.00 cu yd)	1.50
81136	Site Cleanup 1.0 cubic yard	cu yd	1.00	1.50	1.50	Site Cleanup 1.0 cubic yard (1.00 cu yd)	1.50
81137	Final Inspection 1.0 cubic yard	cu yd	1.00	1.50	1.50	Final Inspection 1.0 cubic yard (1.00 cu yd)	1.50
81138	Project Management 1.0 cubic yard	cu yd	1.00	1.50	1.50	Project Management 1.0 cubic yard (1.00 cu yd)	1.50
81139	Permitting 1.0 cubic yard	cu yd	1.00	1.50	1.50	Permitting 1.0 cubic yard (1.00 cu yd)	1.50
81140	Insurance 1.0 cubic yard	cu yd	1.00	1.50	1.50	Insurance 1.0 cubic yard (1.00 cu yd)	1.50
81141	Contingency 1.0 cubic yard	cu yd	1.00	1.50	1.50	Contingency 1.0 cubic yard (1.00 cu yd)	1.50

Table 7.6.4.4: Description of Work Items

SR-100-82, Appendix A.1

Item #	Description	Unit	Quantity	Unit Price	Amount	Details of Work	
						Material	Labor
81142	Excavate 1.0 cubic yard	cu yd	1.00	1.50	1.50	Excavate 1.0 cubic yard (1.00 cu yd)	1.50
81143	Backfill 1.0 cubic yard	cu yd	1.00	1.50	1.50	Backfill 1.0 cubic yard (1.00 cu yd)	1.50
81144	Fill 1.0 cubic yard	cu yd	1.00	1.50	1.50	Fill 1.0 cubic yard (1.00 cu yd)	1.50
81145	Grade 1.0 cubic yard	cu yd	1.00	1.50	1.50	Grade 1.0 cubic yard (1.00 cu yd)	1.50
81146	Compaction 1.0 cubic yard	cu yd	1.00	1.50	1.50	Compaction 1.0 cubic yard (1.00 cu yd)	1.50
81147	Site Preparation 1.0 cubic yard	cu yd	1.00	1.50	1.50	Site Preparation 1.0 cubic yard (1.00 cu yd)	1.50
81148	Remove 1.0 cubic yard	cu yd	1.00	1.50	1.50	Remove 1.0 cubic yard (1.00 cu yd)	1.50
81149	Subgrade 1.0 cubic yard	cu yd	1.00	1.50	1.50	Subgrade 1.0 cubic yard (1.00 cu yd)	1.50
81150	Foundation 1.0 cubic yard	cu yd	1.00	1.50	1.50	Foundation 1.0 cubic yard (1.00 cu yd)	1.50
81151	Structure 1.0 cubic yard	cu yd	1.00	1.50	1.50	Structure 1.0 cubic yard (1.00 cu yd)	1.50
81152	Roofing 1.0 cubic yard	cu yd	1.00	1.50	1.50	Roofing 1.0 cubic yard (1.00 cu yd)	1.50
81153	Interior Finishes 1.0 cubic yard	cu yd	1.00	1.50	1.50	Interior Finishes 1.0 cubic yard (1.00 cu yd)	1.50
81154	Exterior Finishes 1.0 cubic yard	cu yd	1.00	1.50	1.50	Exterior Finishes 1.0 cubic yard (1.00 cu yd)	1.50
81155	Landscaping 1.0 cubic yard	cu yd	1.00	1.50	1.50	Landscaping 1.0 cubic yard (1.00 cu yd)	1.50
81156	Site Cleanup 1.0 cubic yard	cu yd	1.00	1.50	1.50	Site Cleanup 1.0 cubic yard (1.00 cu yd)	1.50
81157	Final Inspection 1.0 cubic yard	cu yd	1.00	1.50	1.50	Final Inspection 1.0 cubic yard (1.00 cu yd)	1.50
81158	Project Management 1.0 cubic yard	cu yd	1.00	1.50	1.50	Project Management 1.0 cubic yard (1.00 cu yd)	1.50
81159	Permitting 1.0 cubic yard	cu yd	1.00	1.50	1.50	Permitting 1.0 cubic yard (1.00 cu yd)	1.50
81160	Insurance 1.0 cubic yard	cu yd	1.00	1.50	1.50	Insurance 1.0 cubic yard (1.00 cu yd)	1.50
81161	Contingency 1.0 cubic yard	cu yd	1.00	1.50	1.50	Contingency 1.0 cubic yard (1.00 cu yd)	1.50

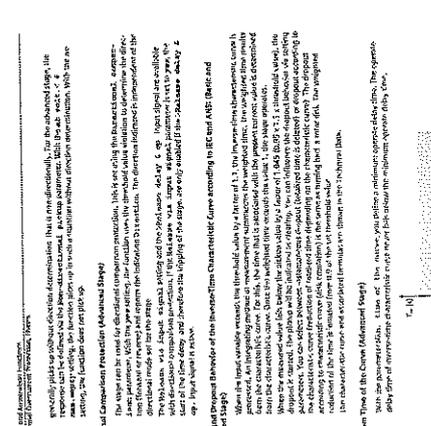
Table 7.6.4.4: Description of Work Items (Continued)

SR-100-82, Appendix A.1

Item #	Description	Unit	Quantity	Unit Price	Amount	Details of Work	
						Material	Labor
81162	Excavate 1.0 cubic yard	cu yd	1.00	1.50	1.50	Excavate 1.0 cubic yard (1.00 cu yd)	1.50
81163	Backfill 1.0 cubic yard	cu yd	1.00	1.50	1.50	Backfill 1.0 cubic yard (1.00 cu yd)	1.50
81164	Fill 1.0 cubic yard	cu yd	1.00	1.50	1.50	Fill 1.0 cubic yard (1.00 cu yd)	1.50
81165	Grade 1.0 cubic yard	cu yd	1.00	1.50	1.50	Grade 1.0 cubic yard (1.00 cu yd)	1.50
81166	Compaction 1.0 cubic yard	cu yd	1.00	1.50	1.50	Compaction 1.0 cubic yard (1.00 cu yd)	1.50
81167	Site Preparation 1.0 cubic yard	cu yd	1.00	1.50	1.50	Site Preparation 1.0 cubic yard (1.00 cu yd)	1.50
81168	Remove 1.0 cubic yard	cu yd	1.00	1.50	1.50	Remove 1.0 cubic yard (1.00 cu yd)	1.50
81169	Subgrade 1.0 cubic yard	cu yd	1.00	1.50	1.50	Subgrade 1.0 cubic yard (1.00 cu yd)	1.50
81170	Foundation 1.0 cubic yard	cu yd	1.00	1.50	1.50	Foundation 1.0 cubic yard (1.00 cu yd)	1.50
81171	Structure 1.0 cubic yard	cu yd	1.00	1.50	1.50	Structure 1.0 cubic yard (1.00 cu yd)	1.50
81172	Roofing 1.0 cubic yard	cu yd	1.00	1.50	1.50	Roofing 1.0 cubic yard (1.00 cu yd)	1.50
81173	Interior Finishes 1.0 cubic yard	cu yd	1.00	1.50	1.50	Interior Finishes 1.0 cubic yard (1.00 cu yd)	1.50
81174	Exterior Finishes 1.0 cubic yard	cu yd	1.00	1.50	1.50	Exterior Finishes 1.0 cubic yard (1.00 cu yd)	1.50
81175	Landscaping 1.0 cubic yard	cu yd	1.00	1.50	1.50	Landscaping 1.0 cubic yard (1.00 cu yd)	1.50
81176	Site Cleanup 1.0 cubic yard	cu yd	1.00	1.50	1.50	Site Cleanup 1.0 cubic yard (1.00 cu yd)	1.50
81177	Final Inspection 1.0 cubic yard	cu yd	1.00	1.50	1.50	Final Inspection 1.0 cubic yard (1.00 cu yd)	1.50
81178	Project Management 1.0 cubic yard	cu yd	1.00	1.50	1.50	Project Management 1.0 cubic yard (1.00 cu yd)	1.50
81179	Permitting 1.0 cubic yard	cu yd	1.00	1.50	1.50	Permitting 1.0 cubic yard (1.00 cu yd)	1.50
81180	Insurance 1.0 cubic yard	cu yd	1.00	1.50	1.50	Insurance 1.0 cubic yard (1.00 cu yd)	1.50
81181	Contingency 1.0 cubic yard	cu yd	1.00	1.50	1.50	Contingency 1.0 cubic yard (1.00 cu yd)	1.50

Table 7.6.4.4: Description of Work Items (Continued)

SR-100-82, Appendix A.1



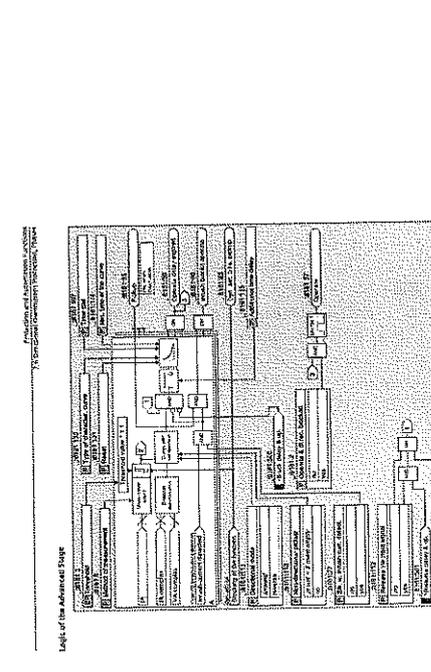
7.6.4.4.1. Description of Work Items

7.6.4.4.2. Description of Work Items

7.6.4.4.3. Description of Work Items

7.6.4.4.4. Description of Work Items

7.6.4.4.5. Description of Work Items



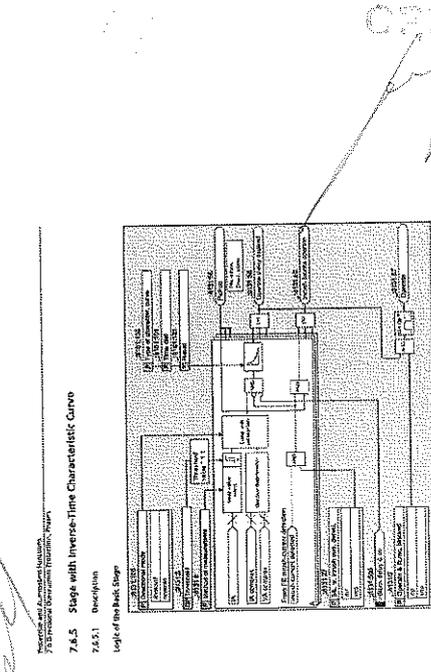
7.6.4.5. Description of Work Items

7.6.4.5.1. Description of Work Items

7.6.4.5.2. Description of Work Items

7.6.4.5.3. Description of Work Items

7.6.4.5.4. Description of Work Items



7.6.4.6. Description of Work Items

7.6.4.6.1. Description of Work Items

7.6.4.6.2. Description of Work Items

7.6.4.6.3. Description of Work Items

SR-100-82, Appendix A.1

7.6.8 Influence of Other Functions via Dynamic Settings

7.6.9 Application Notes for Parallel Lines

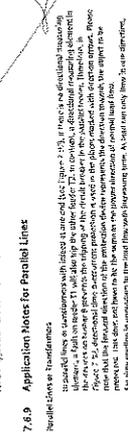


Figure 7-2: Parallel line with transformer

7.6.10 Application Notes for Directional Comparison Protection

7.7 Directional Overcurrent Protection, Ground

7.7.1 Overview of Functions

7.7.2 Structure of the Function

7.7.3 Directional Overcurrent Protection, Ground

7.7.4 Overview of Functions

7.7.5 Structure of the Function

7.7.6 Directional Overcurrent Protection, Ground

7.7.7 Overview of Functions

7.7.8 Structure of the Function

7.7.9 Directional Overcurrent Protection, Ground

7.7.10 Overview of Functions

7.7.11 Structure of the Function

7.7.12 Directional Overcurrent Protection, Ground

7.7.13 Overview of Functions

7.7.14 Structure of the Function

7.7.15 Directional Overcurrent Protection, Ground

7.7.16 Overview of Functions

7.7.17 Structure of the Function

7.7.18 Directional Overcurrent Protection, Ground

7.7.19 Overview of Functions

7.7.20 Structure of the Function

7.7.21 Directional Overcurrent Protection, Ground

7.7.22 Overview of Functions

7.7.23 Structure of the Function

7.6.12 Application and Setting Notes

7.6.13 Application and Setting Notes

7.6.14 Application and Setting Notes

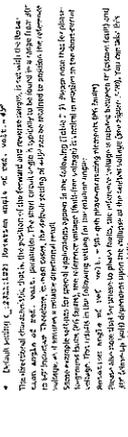


Figure 7-3: Fault on parallel line

7.6.15 Application and Setting Notes

7.6.16 Application and Setting Notes

7.6.17 Application and Setting Notes

7.6.18 Application and Setting Notes

7.6.19 Application and Setting Notes

7.6.20 Application and Setting Notes

7.6.21 Application and Setting Notes

7.6.22 Application and Setting Notes

7.6.23 Application and Setting Notes

7.6.24 Application and Setting Notes

7.6.25 Application and Setting Notes

7.6.26 Application and Setting Notes

7.6.27 Application and Setting Notes

7.6.28 Application and Setting Notes

7.6.29 Application and Setting Notes

7.6.30 Application and Setting Notes

7.6.31 Application and Setting Notes

7.6.32 Application and Setting Notes

7.6.33 Application and Setting Notes

7.6.34 Application and Setting Notes

7.6.35 Application and Setting Notes

7.6.36 Application and Setting Notes

7.6.37 Application and Setting Notes

7.6.38 Application and Setting Notes

7.6.39 Application and Setting Notes

7.6.40 Application and Setting Notes

7.6.41 Application and Setting Notes

7.6.42 Application and Setting Notes



Figure 7-4: Phasor diagram of directional comparison protection

7.6.43 Application and Setting Notes

7.6.44 Application and Setting Notes

7.6.45 Application and Setting Notes

7.6.46 Application and Setting Notes

7.6.47 Application and Setting Notes

7.6.48 Application and Setting Notes

7.6.49 Application and Setting Notes

7.6.50 Application and Setting Notes

7.6.51 Application and Setting Notes

7.6.52 Application and Setting Notes

7.6.53 Application and Setting Notes

7.6.54 Application and Setting Notes

7.6.55 Application and Setting Notes

7.6.56 Application and Setting Notes

7.6.57 Application and Setting Notes

7.6.58 Application and Setting Notes

7.6.59 Application and Setting Notes

7.6.60 Application and Setting Notes

7.6.61 Application and Setting Notes

7.6.62 Application and Setting Notes

7.6.63 Application and Setting Notes

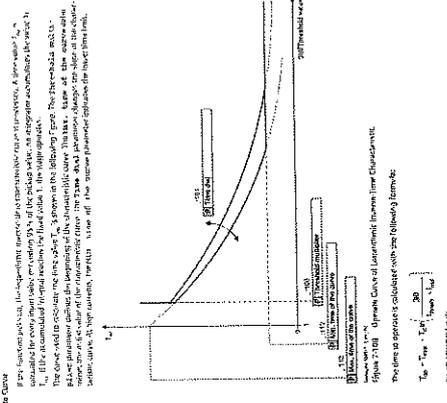
7.6.64 Application and Setting Notes

7.6.65 Application and Setting Notes

7.6.66 Application and Setting Notes

7.6.67 Application and Setting Notes

Outside Curve
 If the curve is a circular curve, the respective curve of an asymptotic curve for the curve. A given value T_{max} is substituted for every other value or every 10% of the value when an asymptotic curve is required for the curve. The curve is then a circular curve with the same radius as the original curve. The asymptotic curve is a curve that approaches the original curve as the value of T_{max} increases. The asymptotic curve is a curve that approaches the original curve as the value of T_{max} increases.



Minimum time of the curve parameter: $L_{min} = L \cdot \frac{T_{min}}{T_{max}}$
 Time of the curve parameter: $L_{max} = L \cdot \frac{T_{max}}{T_{min}}$
 Time of the curve parameter: $L_{avg} = L \cdot \frac{T_{max} + T_{min}}{2}$
 Throughput value (parameter): $T_{avg} = \frac{T_{max} + T_{min}}{2}$
 Throughput value (parameter): $T_{min} = \frac{T_{max} - T_{min}}{2}$
 Throughput value (parameter): $T_{max} = \frac{T_{max} + T_{min}}{2}$
 If the calculated time of the curve is T_{avg} , the curve is a circular curve.

General information about the curve:
 - Curve length: L
 - Minimum time delay: T_{min}
 - Maximum time delay: T_{max}
 - Average time delay: T_{avg}

7.7.2.2 Asymptotic Curve
 The asymptotic curve is a curve that approaches the original curve as the value of T_{max} increases. The asymptotic curve is a curve that approaches the original curve as the value of T_{max} increases.

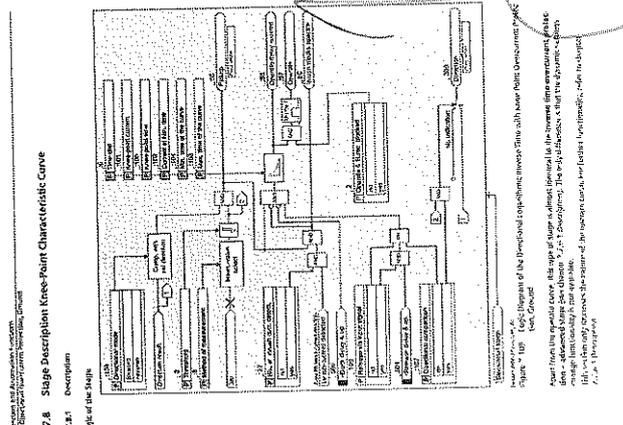
7.7.2.3 Dynamic Equations
 The dynamic equations are used to calculate the time delay of the curve. The dynamic equations are used to calculate the time delay of the curve. The dynamic equations are used to calculate the time delay of the curve.

7.7.2.4 Information List
 The information list contains the following information:
 - Curve length: L
 - Minimum time delay: T_{min}
 - Maximum time delay: T_{max}
 - Average time delay: T_{avg}

7.7.2.5 Stages Description
 The stages description is used to describe the stages of the curve. The stages description is used to describe the stages of the curve.

Stage	Description	Parameter	Unit
1	Initial delay	T_{min}	ms
2	Propagation delay	T_{prop}	ms
3	Processing delay	T_{proc}	ms
4	Queueing delay	T_{queue}	ms
5	Service delay	T_{serv}	ms
6	Output delay	T_{out}	ms
7	Total delay	T_{total}	ms

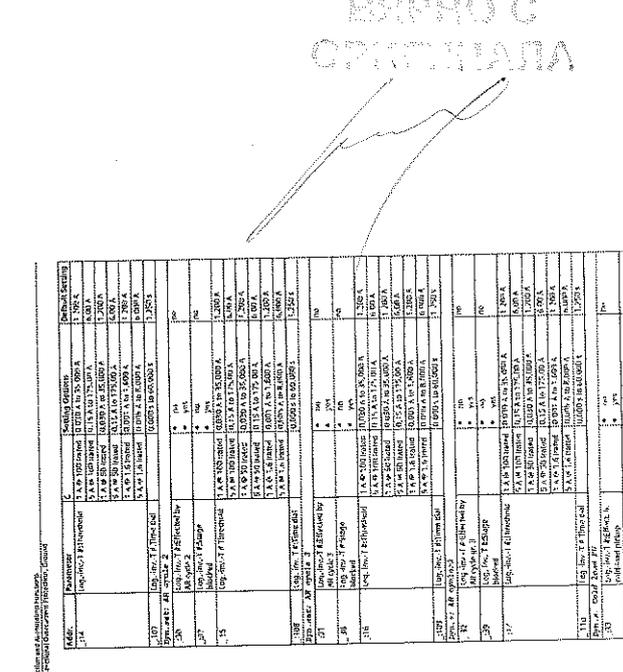
7.7.2.6 Stage Description
 The stage description is used to describe the stages of the curve. The stage description is used to describe the stages of the curve.



7.7.2.7 Stages Description
 The stages description is used to describe the stages of the curve. The stages description is used to describe the stages of the curve.

Stage	Description	Parameter	Unit
1	Initial delay	T_{min}	ms
2	Propagation delay	T_{prop}	ms
3	Processing delay	T_{proc}	ms
4	Queueing delay	T_{queue}	ms
5	Service delay	T_{serv}	ms
6	Output delay	T_{out}	ms
7	Total delay	T_{total}	ms

7.7.2.8 Stage Description
 The stage description is used to describe the stages of the curve. The stage description is used to describe the stages of the curve.



Size the delays based on the delay parameters in the table below. If the delay is 0, the delay is not applicable. The delay is the time it takes for the signal to propagate through the system.

Parameter Name	Value
Delay 1	0
Delay 2	0
Delay 3	0
Delay 4	0
Delay 5	0
Delay 6	0
Delay 7	0
Delay 8	0
Delay 9	0
Delay 10	0
Delay 11	0
Delay 12	0
Delay 13	0
Delay 14	0
Delay 15	0
Delay 16	0
Delay 17	0
Delay 18	0
Delay 19	0
Delay 20	0
Delay 21	0
Delay 22	0
Delay 23	0
Delay 24	0
Delay 25	0
Delay 26	0
Delay 27	0
Delay 28	0
Delay 29	0
Delay 30	0
Delay 31	0
Delay 32	0

7.13.9 Settings

Parameter #	Parameter	Default Setting
1	Supply Voltage	5V
2	Supply Voltage Error	0.05V
3	Supply Voltage Error Limit	0.05V
4	Supply Voltage Error Time	0.05s
5	Supply Voltage Error Time Limit	0.05s
6	Supply Voltage Error Time Limit	0.05s
7	Supply Voltage Error Time Limit	0.05s
8	Supply Voltage Error Time Limit	0.05s
9	Supply Voltage Error Time Limit	0.05s
10	Supply Voltage Error Time Limit	0.05s
11	Supply Voltage Error Time Limit	0.05s
12	Supply Voltage Error Time Limit	0.05s
13	Supply Voltage Error Time Limit	0.05s
14	Supply Voltage Error Time Limit	0.05s
15	Supply Voltage Error Time Limit	0.05s
16	Supply Voltage Error Time Limit	0.05s
17	Supply Voltage Error Time Limit	0.05s
18	Supply Voltage Error Time Limit	0.05s
19	Supply Voltage Error Time Limit	0.05s
20	Supply Voltage Error Time Limit	0.05s
21	Supply Voltage Error Time Limit	0.05s
22	Supply Voltage Error Time Limit	0.05s
23	Supply Voltage Error Time Limit	0.05s
24	Supply Voltage Error Time Limit	0.05s
25	Supply Voltage Error Time Limit	0.05s
26	Supply Voltage Error Time Limit	0.05s
27	Supply Voltage Error Time Limit	0.05s
28	Supply Voltage Error Time Limit	0.05s
29	Supply Voltage Error Time Limit	0.05s
30	Supply Voltage Error Time Limit	0.05s
31	Supply Voltage Error Time Limit	0.05s
32	Supply Voltage Error Time Limit	0.05s

7.13.10 Information List

Parameter #	Information	Unit Class (Type)	Type
1	Supply Voltage Error	0.05V	U
2	Supply Voltage Error Time Limit	0.05s	U
3	Supply Voltage Error Time Limit	0.05s	U
4	Supply Voltage Error Time Limit	0.05s	U
5	Supply Voltage Error Time Limit	0.05s	U
6	Supply Voltage Error Time Limit	0.05s	U
7	Supply Voltage Error Time Limit	0.05s	U
8	Supply Voltage Error Time Limit	0.05s	U
9	Supply Voltage Error Time Limit	0.05s	U
10	Supply Voltage Error Time Limit	0.05s	U
11	Supply Voltage Error Time Limit	0.05s	U
12	Supply Voltage Error Time Limit	0.05s	U
13	Supply Voltage Error Time Limit	0.05s	U
14	Supply Voltage Error Time Limit	0.05s	U
15	Supply Voltage Error Time Limit	0.05s	U
16	Supply Voltage Error Time Limit	0.05s	U
17	Supply Voltage Error Time Limit	0.05s	U
18	Supply Voltage Error Time Limit	0.05s	U
19	Supply Voltage Error Time Limit	0.05s	U
20	Supply Voltage Error Time Limit	0.05s	U
21	Supply Voltage Error Time Limit	0.05s	U
22	Supply Voltage Error Time Limit	0.05s	U
23	Supply Voltage Error Time Limit	0.05s	U
24	Supply Voltage Error Time Limit	0.05s	U
25	Supply Voltage Error Time Limit	0.05s	U
26	Supply Voltage Error Time Limit	0.05s	U
27	Supply Voltage Error Time Limit	0.05s	U
28	Supply Voltage Error Time Limit	0.05s	U
29	Supply Voltage Error Time Limit	0.05s	U
30	Supply Voltage Error Time Limit	0.05s	U
31	Supply Voltage Error Time Limit	0.05s	U
32	Supply Voltage Error Time Limit	0.05s	U

7.14.3 Stage Description

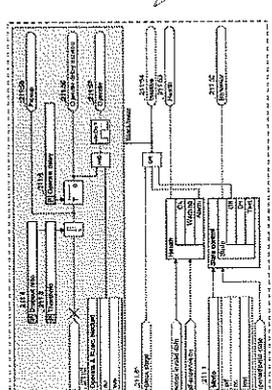


Figure 7-137: Block Diagram of a Stage: Overvoltage Protection with Positive Sequence Voltage

This block diagram shows the internal structure of the stage. It includes a feedback loop and a delay element. The diagram is a block diagram with various inputs and outputs, and internal components like comparators and logic gates.

7.14.4 Application and Setting Notes

- Default setting, L_011.1: The value is 0.1.
- The threshold is set according to the condition of the positive sequence voltage.
- Specify the threshold value to avoid false operation.
- Default L_011.9: The value is 0.1.
- The operation is determined by the specific condition.

7.14 Overview of Functions

The function overview table provides a detailed description of the system's functions. It includes a table with columns for function name, description, and status.

- Overvoltage Protection with Positive Sequence Voltage
- Overvoltage Protection with Negative Sequence Voltage
- Overvoltage Protection with Zero Sequence Voltage

7.14.2 Structure of the Function

The structure of the function is shown in a block diagram. It illustrates the flow of data and control signals between different components of the system.



Figure 7-136: Structure/Block Diagram of the Function

Parameter #

Parameter #	Information	Unit Class (Type)	Type
1	Supply Voltage Error	0.05V	U
2	Supply Voltage Error Time Limit	0.05s	U
3	Supply Voltage Error Time Limit	0.05s	U
4	Supply Voltage Error Time Limit	0.05s	U
5	Supply Voltage Error Time Limit	0.05s	U
6	Supply Voltage Error Time Limit	0.05s	U
7	Supply Voltage Error Time Limit	0.05s	U
8	Supply Voltage Error Time Limit	0.05s	U
9	Supply Voltage Error Time Limit	0.05s	U
10	Supply Voltage Error Time Limit	0.05s	U
11	Supply Voltage Error Time Limit	0.05s	U
12	Supply Voltage Error Time Limit	0.05s	U
13	Supply Voltage Error Time Limit	0.05s	U
14	Supply Voltage Error Time Limit	0.05s	U
15	Supply Voltage Error Time Limit	0.05s	U
16	Supply Voltage Error Time Limit	0.05s	U
17	Supply Voltage Error Time Limit	0.05s	U
18	Supply Voltage Error Time Limit	0.05s	U
19	Supply Voltage Error Time Limit	0.05s	U
20	Supply Voltage Error Time Limit	0.05s	U
21	Supply Voltage Error Time Limit	0.05s	U
22	Supply Voltage Error Time Limit	0.05s	U
23	Supply Voltage Error Time Limit	0.05s	U
24	Supply Voltage Error Time Limit	0.05s	U
25	Supply Voltage Error Time Limit	0.05s	U
26	Supply Voltage Error Time Limit	0.05s	U
27	Supply Voltage Error Time Limit	0.05s	U
28	Supply Voltage Error Time Limit	0.05s	U
29	Supply Voltage Error Time Limit	0.05s	U
30	Supply Voltage Error Time Limit	0.05s	U
31	Supply Voltage Error Time Limit	0.05s	U
32	Supply Voltage Error Time Limit	0.05s	U

7.14.5 Settings

Parameter #	Parameter	Default Setting	Unit Class (Type)	Type
1	Stage 1 Delay	0.05s	U	U
2	Stage 1 Delay Error	0.005s	U	U
3	Stage 1 Delay Error Limit	0.005s	U	U
4	Stage 1 Delay Error Time Limit	0.005s	U	U
5	Stage 1 Delay Error Time Limit	0.005s	U	U
6	Stage 1 Delay Error Time Limit	0.005s	U	U
7	Stage 1 Delay Error Time Limit	0.005s	U	U
8	Stage 1 Delay Error Time Limit	0.005s	U	U
9	Stage 1 Delay Error Time Limit	0.005s	U	U
10	Stage 1 Delay Error Time Limit	0.005s	U	U
11	Stage 1 Delay Error Time Limit	0.005s	U	U
12	Stage 1 Delay Error Time Limit	0.005s	U	U
13	Stage 1 Delay Error Time Limit	0.005s	U	U
14	Stage 1 Delay Error Time Limit	0.005s	U	U
15	Stage 1 Delay Error Time Limit	0.005s	U	U
16	Stage 1 Delay Error Time Limit	0.005s	U	U
17	Stage 1 Delay Error Time Limit	0.005s	U	U
18	Stage 1 Delay Error Time Limit	0.005s	U	U
19	Stage 1 Delay Error Time Limit	0.005s	U	U
20	Stage 1 Delay Error Time Limit	0.005s	U	U
21	Stage 1 Delay Error Time Limit	0.005s	U	U
22	Stage 1 Delay Error Time Limit	0.005s	U	U
23	Stage 1 Delay Error Time Limit	0.005s	U	U
24	Stage 1 Delay Error Time Limit	0.005s	U	U
25	Stage 1 Delay Error Time Limit	0.005s	U	U
26	Stage 1 Delay Error Time Limit	0.005s	U	U
27	Stage 1 Delay Error Time Limit	0.005s	U	U
28	Stage 1 Delay Error Time Limit	0.005s	U	U
29	Stage 1 Delay Error Time Limit	0.005s	U	U
30	Stage 1 Delay Error Time Limit	0.005s	U	U
31	Stage 1 Delay Error Time Limit	0.005s	U	U
32	Stage 1 Delay Error Time Limit	0.005s	U	U

7.14.6 Information List

Parameter #	Information	Unit Class (Type)	Type
1	Supply Voltage Error	0.05V	U
2	Supply Voltage Error Time Limit	0.05s	U
3	Supply Voltage Error Time Limit	0.05s	U
4	Supply Voltage Error Time Limit	0.05s	U
5	Supply Voltage Error Time Limit	0.05s	U
6	Supply Voltage Error Time Limit	0.05s	U
7	Supply Voltage Error Time Limit	0.05s	U
8	Supply Voltage Error Time Limit	0.05s	U
9	Supply Voltage Error Time Limit	0.05s	U
10	Supply Voltage Error Time Limit	0.05s	U
11	Supply Voltage Error Time Limit	0.05s	U
12	Supply Voltage Error Time Limit	0.05s	U
13	Supply Voltage Error Time Limit	0.05s	U
14	Supply Voltage Error Time Limit	0.05s	U
15	Supply Voltage Error Time Limit	0.05s	U
16	Supply Voltage Error Time Limit	0.05s	U
17	Supply Voltage Error Time Limit	0.05s	U
18	Supply Voltage Error Time Limit	0.05s	U
19	Supply Voltage Error Time Limit	0.05s	U
20	Supply Voltage Error Time Limit	0.05s	U
21	Supply Voltage Error Time Limit	0.05s	U
22	Supply Voltage Error Time Limit	0.05s	U
23	Supply Voltage Error Time Limit	0.05s	U
24	Supply Voltage Error Time Limit	0.05s	U
25	Supply Voltage Error Time Limit	0.05s	U
26	Supply Voltage Error Time Limit	0.05s	U
27	Supply Voltage Error Time Limit	0.05s	U
28	Supply Voltage Error Time Limit	0.05s	U
29	Supply Voltage Error Time Limit	0.05s	U
30	Supply Voltage Error Time Limit	0.05s	U
31	Supply Voltage Error Time Limit	0.05s	U
32	Supply Voltage Error Time Limit	0.05s	U

7.16.5 Information

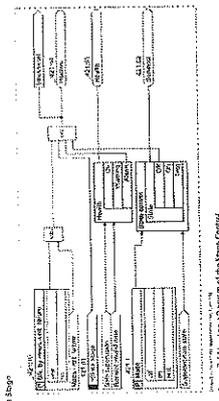


Figure 7.141 Logic Diagram of the Stage Control

87100

869

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7.16.6 Information List

No.	Information	Pin	Chk	Type
7.16.6.1	Phase 1 (Sine)	1	1	S
7.16.6.2	Phase 2 (Sine)	2	1	S
7.16.6.3	Phase 3 (Sine)	3	1	S
7.16.6.4	Phase 4 (Sine)	4	1	S
7.16.6.5	Phase 5 (Sine)	5	1	S
7.16.6.6	Phase 6 (Sine)	6	1	S

7.16.5 Settings

Mod.	Parameter	C	Settings	Default	Setting
7.16.5.1	Control Voltage	0	120	0	120
7.16.5.2	Control Voltage	0	120	0	120
7.16.5.3	Control Voltage	0	120	0	120
7.16.5.4	Control Voltage	0	120	0	120

869

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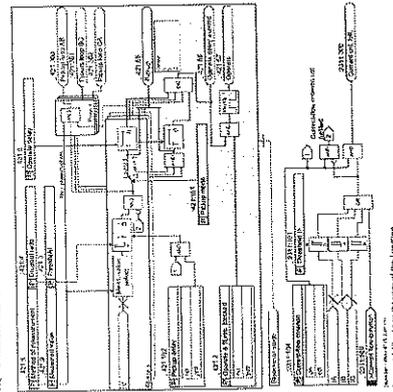


Figure 7.142 Logic Diagram of the Information

87100

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7.16.7 Description

No.	Information	Pin	Chk	Type
7.16.7.1	Phase 1 (Sine)	1	1	S
7.16.7.2	Phase 2 (Sine)	2	1	S
7.16.7.3	Phase 3 (Sine)	3	1	S
7.16.7.4	Phase 4 (Sine)	4	1	S
7.16.7.5	Phase 5 (Sine)	5	1	S
7.16.7.6	Phase 6 (Sine)	6	1	S

7.16.6 Information List

No.	Information	Pin	Chk	Type
7.16.6.1	Phase 1 (Sine)	1	1	S
7.16.6.2	Phase 2 (Sine)	2	1	S
7.16.6.3	Phase 3 (Sine)	3	1	S
7.16.6.4	Phase 4 (Sine)	4	1	S
7.16.6.5	Phase 5 (Sine)	5	1	S
7.16.6.6	Phase 6 (Sine)	6	1	S

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7.16.5

87100

7.16.6

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7.16.7

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7.16.4 Application and Setting Notes

- Parameter to be set...
- Control Voltage...
- Control Voltage...
- Control Voltage...

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7.16.7 Description

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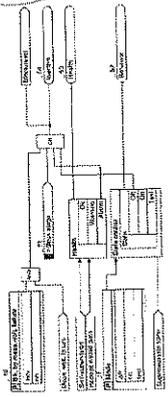


Figure 7.143 Logic Diagram of the Description

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7.18.3 Overfrequency Protection Stage

stage of a stage

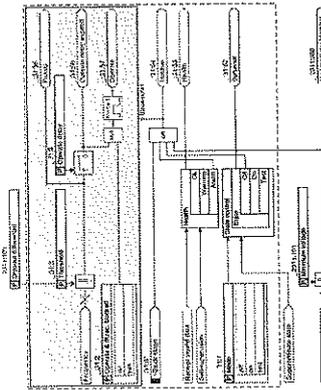


Figure 7.18.3 Logic Diagram of the Overfrequency Protection Stage

Frequency Measurement Method

Overfrequency protection is available in 2 protection functions. These work with different frequency measurement methods. The protection functions are:

- Protection function 1 (P1)
- Protection function 2 (P2)

The protection functions are implemented in a similar way. The protection functions are implemented in a similar way. The protection functions are implemented in a similar way.

7.18 Overview of Functions

7.18.1 Overview of Functions

- The overfrequency protection functions (OFR1/OFR2)
- Monitor the frequency level and trigger protection actions
- Detects overfrequency during the power frequency
- Protects additional protection functions of the power frequency
- Frequency protection is implemented in two protection functions (P1/P2)
- Frequency protection is implemented in two protection functions (P1/P2)
- Frequency protection is implemented in two protection functions (P1/P2)
- Frequency protection is implemented in two protection functions (P1/P2)

7.18.2 Structure of the Function

The overfrequency protection function is based on protection functions, which are based on voltage measurement. The protection functions are implemented in a similar way. The protection functions are implemented in a similar way.



Figure 7.18.2 Structure of the Function

7.19 Underfrequency Protection

7.19.1 Overview of Functions

The underfrequency protection function (UFR) is used to:

- Detect underfrequency in electrical power systems or motors
- Measure power systems
- Load shedding to ensure system stability and prevent blackouts
- Measure power systems
- Measure power systems
- Measure power systems
- Measure power systems

7.19.2 Structure of the Function

The underfrequency protection function is based on voltage measurement. The protection functions are implemented in a similar way. The protection functions are implemented in a similar way.

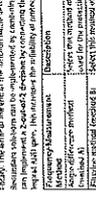


Figure 7.19.2 Structure of the Function

7.2.4.1 Application and Setting Notes

- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}

7.2.4.5 Settings

Item	Parameter	Setting System	Default Setting
General	General Settings	• L_{1300} (Reevaluation)	20
	General Settings	• Q_{1300} (Reevaluation)	20
Protection	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20

NOTE: The values of the parameters are subject to change without notice.

7.2.4.6 Application and Setting Notes

- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}

7.2.4.5 Settings

Item	Parameter	Setting System	Default Setting
General	General Settings	• L_{1300} (Reevaluation)	20
	General Settings	• Q_{1300} (Reevaluation)	20
Protection	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20

NOTE: The values of the parameters are subject to change without notice.

7.2.4.1 Description

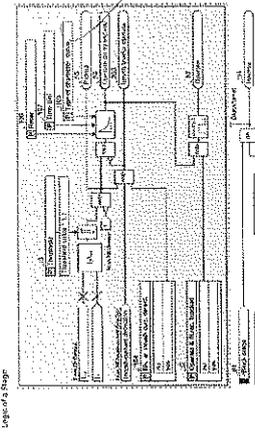


Figure 7.11. Logic Diagram of the Negative Sequence Protection with Inverse Time Characteristic Curve

Method of Measurement

The measurement procedure is as follows: The test system is set up as shown in the diagram. The test system is then energized and the test is performed. The test results are then recorded and the test is repeated for different test conditions.

History and Change of the Inverse Time Characteristic Curve according to IEC 60255

The inverse time characteristic curve is defined by the equation: $t = \frac{K}{I^n}$, where t is the operating time, K is the time constant, and I is the fault current. The curve is used to determine the operating time of the protection device for a given fault current. The curve is defined by the equation: $t = \frac{K}{I^n}$, where t is the operating time, K is the time constant, and I is the fault current.

NOTE: The values of the parameters are subject to change without notice.

7.2.5 Thermal Overload Protection, 3-Phase

7.2.5.1 Overview of Functions

- Thermal overload protection function (AOS) is used to protect the equipment (motor, transformer, capacitor, inductor, and other equipment) from thermal damage.
- The thermal overload protection function (AOS) is used to protect the equipment (motor, transformer, capacitor, inductor, and other equipment) from thermal damage.

7.2.5.2 Structure of the Function

The thermal overload protection function (AOS) is used to protect the equipment (motor, transformer, capacitor, inductor, and other equipment) from thermal damage. The function is used to protect the equipment (motor, transformer, capacitor, inductor, and other equipment) from thermal damage.

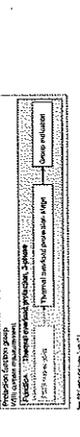


Figure 7.12. Structure of the Function

7.2.5.3 Filter for RMS Value Gain

- The filter is used to filter the RMS value gain. The filter is used to filter the RMS value gain.
- The filter is used to filter the RMS value gain. The filter is used to filter the RMS value gain.

NOTE: The values of the parameters are subject to change without notice.

7.2.4.9 Settings

Item	Parameter	Setting System	Default Setting
General	General Settings	• L_{1300} (Reevaluation)	20
	General Settings	• Q_{1300} (Reevaluation)	20
Protection	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20

7.2.4.10 Information List

Item	Parameter	Setting System	Default Setting
General	General Settings	• L_{1300} (Reevaluation)	20
	General Settings	• Q_{1300} (Reevaluation)	20
Protection	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20

NOTE: The values of the parameters are subject to change without notice.

7.2.4.1 Application and Setting Notes

- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}

7.2.4.5 Settings

Item	Parameter	Setting System	Default Setting
General	General Settings	• L_{1300} (Reevaluation)	20
	General Settings	• Q_{1300} (Reevaluation)	20
Protection	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20

NOTE: The values of the parameters are subject to change without notice.

7.2.4.6 Application and Setting Notes

- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}
- Reevaluation setting value: $L_{1300} = 20$, threshold = 20
- L_{1300} is the value of the evaluation function. A threshold value of 10 is a suitable value with a tolerance of 10%.
- Parameter: Q_{1300}

7.2.4.5 Settings

Item	Parameter	Setting System	Default Setting
General	General Settings	• L_{1300} (Reevaluation)	20
	General Settings	• Q_{1300} (Reevaluation)	20
Protection	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20
	Protection Settings	• L_{1300} (Reevaluation)	20
	Protection Settings	• Q_{1300} (Reevaluation)	20

NOTE: The values of the parameters are subject to change without notice.

7.2.7.6 Information List

No.	Identifier	Group	Type
21107-3	Control Room	U13	P
21107-4	Control Room	U13	P
21107-5	Control Room	U13	P
21107-6	Control Room	U13	P
21107-7	Control Room	U13	P
21107-8	Control Room	U13	P
21107-9	Control Room	U13	P
21107-10	Control Room	U13	P
21107-11	Control Room	U13	P
21107-12	Control Room	U13	P
21107-13	Control Room	U13	P
21107-14	Control Room	U13	P
21107-15	Control Room	U13	P
21107-16	Control Room	U13	P
21107-17	Control Room	U13	P
21107-18	Control Room	U13	P
21107-19	Control Room	U13	P
21107-20	Control Room	U13	P
21107-21	Control Room	U13	P
21107-22	Control Room	U13	P
21107-23	Control Room	U13	P
21107-24	Control Room	U13	P
21107-25	Control Room	U13	P
21107-26	Control Room	U13	P
21107-27	Control Room	U13	P
21107-28	Control Room	U13	P
21107-29	Control Room	U13	P
21107-30	Control Room	U13	P

207

No.	Identifier	Data Class	Type
21107-31	Control Room	U13	P
21107-32	Control Room	U13	P
21107-33	Control Room	U13	P
21107-34	Control Room	U13	P
21107-35	Control Room	U13	P
21107-36	Control Room	U13	P
21107-37	Control Room	U13	P
21107-38	Control Room	U13	P
21107-39	Control Room	U13	P
21107-40	Control Room	U13	P
21107-41	Control Room	U13	P
21107-42	Control Room	U13	P
21107-43	Control Room	U13	P
21107-44	Control Room	U13	P
21107-45	Control Room	U13	P
21107-46	Control Room	U13	P
21107-47	Control Room	U13	P
21107-48	Control Room	U13	P
21107-49	Control Room	U13	P
21107-50	Control Room	U13	P
21107-51	Control Room	U13	P
21107-52	Control Room	U13	P
21107-53	Control Room	U13	P
21107-54	Control Room	U13	P
21107-55	Control Room	U13	P
21107-56	Control Room	U13	P
21107-57	Control Room	U13	P
21107-58	Control Room	U13	P
21107-59	Control Room	U13	P
21107-60	Control Room	U13	P
21107-61	Control Room	U13	P
21107-62	Control Room	U13	P
21107-63	Control Room	U13	P
21107-64	Control Room	U13	P
21107-65	Control Room	U13	P
21107-66	Control Room	U13	P
21107-67	Control Room	U13	P
21107-68	Control Room	U13	P
21107-69	Control Room	U13	P
21107-70	Control Room	U13	P

208

No.	Identifier	Data Class	Type
21107-71	Control Room	U13	P
21107-72	Control Room	U13	P
21107-73	Control Room	U13	P
21107-74	Control Room	U13	P
21107-75	Control Room	U13	P
21107-76	Control Room	U13	P
21107-77	Control Room	U13	P
21107-78	Control Room	U13	P
21107-79	Control Room	U13	P
21107-80	Control Room	U13	P
21107-81	Control Room	U13	P
21107-82	Control Room	U13	P
21107-83	Control Room	U13	P
21107-84	Control Room	U13	P
21107-85	Control Room	U13	P
21107-86	Control Room	U13	P
21107-87	Control Room	U13	P
21107-88	Control Room	U13	P
21107-89	Control Room	U13	P
21107-90	Control Room	U13	P
21107-91	Control Room	U13	P
21107-92	Control Room	U13	P
21107-93	Control Room	U13	P
21107-94	Control Room	U13	P
21107-95	Control Room	U13	P
21107-96	Control Room	U13	P
21107-97	Control Room	U13	P
21107-98	Control Room	U13	P
21107-99	Control Room	U13	P
21107-100	Control Room	U13	P

209

7.2.8 Arc Protection

7.2.8.1 Overview of Function
 The function for protection ...
 • Detects arcs in an installation ...
 • Initiates system shutdown ...
 • Protects system from thermal overheat ...
 • Improves safety of employees ...
 • Triggers a safe way ...
 • Is suitable for use in all voltage levels

7.2.8.2 Structure of the Function

The arc protection function can be broken down into the following steps:
 • General ...
 • General ...
 • General ...

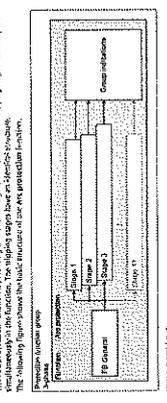


Figure 7.192: Structure of the Arc Protection Function

7.2.8.3 Function Description

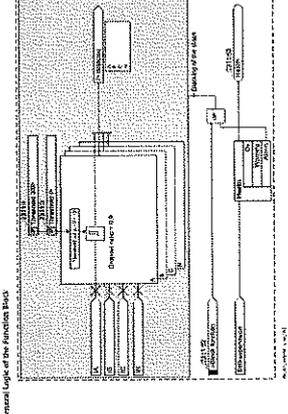


Figure 7.193: General Logic Diagram of the Function Block

7.2.8.4 Logic of the Block

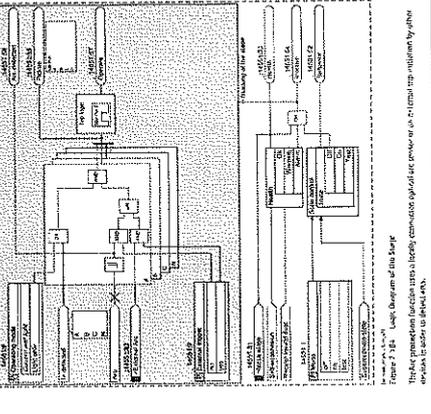


Figure 7.194: Logic Diagram of the Block

NOTE: One or more components in this system may be replaced by a component of a different type without affecting the safety of the system.

NOTE: One or more components in this system may be replaced by a component of a different type without affecting the safety of the system.

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Handwritten signature

Handwritten signature

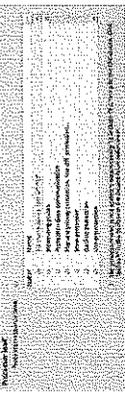
378

8.2 Resource Consumption Supervision

8.2.1 Load Model

Figure 8-3: View of the Load Model. A load model is a hierarchical model that describes the load on a system. It is used to estimate the load on a system and to determine the resources required to support the load. The load model is composed of the following elements:

- Load Model:** The overall model representing the load on the system.
- Component:** Individual parts of the system that generate or consume resources.
- Resource:** The specific resources required by the components, such as CPU, memory, and I/O.
- Relationship:** The dependencies between components and resources.



The load model is used to estimate the load on a system and to determine the resources required to support the load. It is a hierarchical model that describes the load on a system. The load model is composed of the following elements:

- Load Model:** The overall model representing the load on the system.
- Component:** Individual parts of the system that generate or consume resources.
- Resource:** The specific resources required by the components, such as CPU, memory, and I/O.
- Relationship:** The dependencies between components and resources.

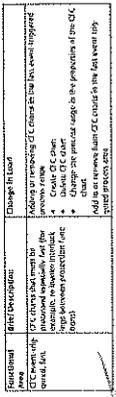
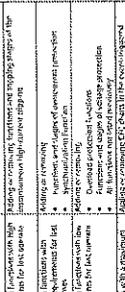


Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

8.2 Function Points

When you use a function point (FP) analysis, you are measuring a function point (FP) as a unit of additional work. A function point is a unit of work that is used to measure the effort required to develop a system. The function point analysis is a metric that is used to estimate the effort required to develop a system. The function point analysis is based on the following factors:

- Internal Function Points (IFP):** The number of function points within the system boundaries.
- External Function Points (EFP):** The number of function points that interact with the system.



The function point analysis is a metric that is used to estimate the effort required to develop a system. The function point analysis is based on the following factors:

- Internal Function Points (IFP):** The number of function points within the system boundaries.
- External Function Points (EFP):** The number of function points that interact with the system.

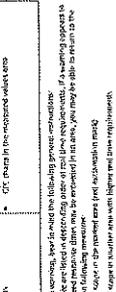


Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

8.2.3 CFC Resources

A CFC resource is a resource that is used by a CFC. The CFC resources are used to estimate the effort required to develop a system. The CFC resources are based on the following factors:

- CPU:** The number of CPU cycles required to execute the CFC.
- Memory:** The amount of memory required to execute the CFC.
- I/O:** The amount of I/O required to execute the CFC.
- Network:** The amount of network traffic required to execute the CFC.



Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

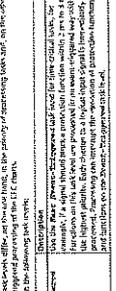


Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

8.3 Supervision of the Secondary System

Task List	Description
Create CFCs	Create CFCs for the system.
Configure CFCs	Configure CFCs for the system.
Monitor CFCs	Monitor CFCs for the system.
Manage CFCs	Manage CFCs for the system.

The CFCs are used to estimate the effort required to develop a system. The CFCs are based on the following factors:

- CPU:** The number of CPU cycles required to execute the CFC.
- Memory:** The amount of memory required to execute the CFC.
- I/O:** The amount of I/O required to execute the CFC.
- Network:** The amount of network traffic required to execute the CFC.



Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.



Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

8.3.1 Overview

The CFCs are used to estimate the effort required to develop a system. The CFCs are based on the following factors:

- CPU:** The number of CPU cycles required to execute the CFC.
- Memory:** The amount of memory required to execute the CFC.
- I/O:** The amount of I/O required to execute the CFC.
- Network:** The amount of network traffic required to execute the CFC.



Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

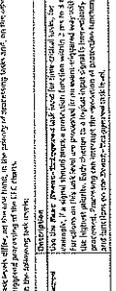


Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

8.3.2 Measuring Voltage Failure

The Measuring Voltage Failure function point is used to measure the voltage failure. The voltage failure is a function point that is used to estimate the effort required to develop a system. The voltage failure is based on the following factors:

- Voltage:** The voltage level of the system.
- Failure:** The number of voltage failures.



Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

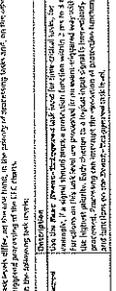


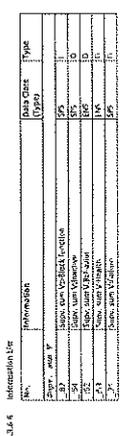
Figure 8-3: View of the Load Model. A diagram showing the relationship between a Load Model, Components, and Resources. The diagram illustrates how the Load Model is composed of multiple Components, which are further divided into Resources.

8.2.1.5 Settings

Addr.	Parameter	Setting Domain	Default Setting
1	Supp. Bus Voltage	0 V to 10 V	0 V
2	Supp. Bus Threshold	0.950 to 1.050	1.000
3	Supp. Bus Alarm	Supp. Bus Alarm	0

8.2.1.6 Information List

No.	Information	Data Size (Bits)	Type
1	Supp. Bus Voltage	16	U
2	Supp. Bus Threshold	16	U
3	Supp. Bus Alarm	16	U



8.2.1.7 Overview of Function

The Voltage Phase-Retention Supervisor (VPRS) monitors the phase sequence of the secondary current sensors to ensure the sequence of the secondary current sensors is correct. This enables the VPRS to detect a phase sequence error and to generate a phase sequence error alarm.

8.2.1.8 Structure of the Function

The Voltage Phase-Retention Supervisor function is located in the Power System data of each 1-phase current sensor.



8.2.1.9 Structure of the Function

The Voltage Phase-Retention Supervisor function is located in the Power System data of each 1-phase current sensor.

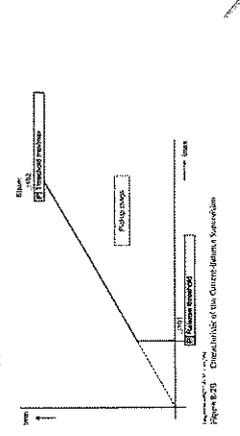


Figure 8.2.1 - Structure of the Voltage Phase-Retention Supervisor

8.2.1.10 Settings

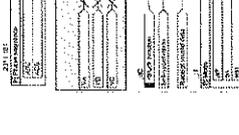
Addr.	Parameter	Setting Domain	Default Setting
1	Supp. Bus Voltage	0 V to 10 V	0 V
2	Supp. Bus Threshold	0.950 to 1.050	1.000
3	Supp. Bus Alarm	Supp. Bus Alarm	0

8.2.1.11 Information List

No.	Information	Data Size (Bits)	Type
1	Supp. Bus Voltage	16	U
2	Supp. Bus Threshold	16	U
3	Supp. Bus Alarm	16	U

8.2.2.1 Function Description

The function description details the operation of the Voltage Phase-Retention Supervisor, including the detection of phase sequence errors and the generation of alarms.

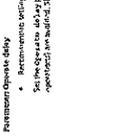


8.2.2.2 Overview of Function

The Voltage Phase-Retention Supervisor (VPRS) monitors the phase sequence of the secondary current sensors to ensure the sequence of the secondary current sensors is correct. This enables the VPRS to detect a phase sequence error and to generate a phase sequence error alarm.

8.2.2.3 Structure of the Function

The Voltage Phase-Retention Supervisor function is located in the Power System data of each 1-phase current sensor.



8.2.2.4 Structure of the Function

The Voltage Phase-Retention Supervisor function is located in the Power System data of each 1-phase current sensor.

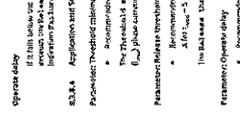


Figure 8.2.2 - Structure of the Voltage Phase-Retention Supervisor

8.2.2.5 Settings

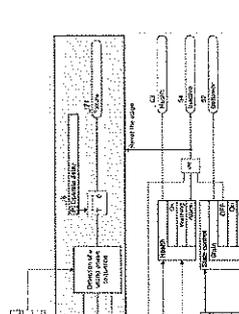
Addr.	Parameter	Setting Domain	Default Setting
1	Supp. Bus Voltage	0 V to 10 V	0 V
2	Supp. Bus Threshold	0.950 to 1.050	1.000
3	Supp. Bus Alarm	Supp. Bus Alarm	0

8.2.2.6 Information List

No.	Information	Data Size (Bits)	Type
1	Supp. Bus Voltage	16	U
2	Supp. Bus Threshold	16	U
3	Supp. Bus Alarm	16	U

8.2.3.1 Function Description

The function description details the operation of the Current-Sum Supervision, including the detection of current sum errors and the generation of alarms.

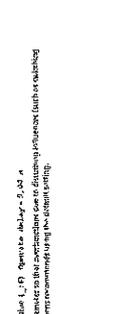


8.2.3.2 Overview of Function

The Current-Sum Supervision (CSS) monitors the sum of the secondary current sensors to ensure the sum is correct. This enables the CSS to detect a current sum error and to generate a current sum error alarm.

8.2.3.3 Structure of the Function

The Current-Sum Supervision function is located in the Power System data of each 1-phase current sensor.



8.2.3.4 Structure of the Function

The Current-Sum Supervision function is located in the Power System data of each 1-phase current sensor.

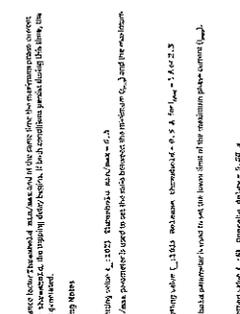


Figure 8.2.3 - Structure of the Current-Sum Supervision

8.2.3.5 Settings

Addr.	Parameter	Setting Domain	Default Setting
1	Supp. Bus Voltage	0 V to 10 V	0 V
2	Supp. Bus Threshold	0.950 to 1.050	1.000
3	Supp. Bus Alarm	Supp. Bus Alarm	0

8.2.3.6 Information List

No.	Information	Data Size (Bits)	Type
1	Supp. Bus Voltage	16	U
2	Supp. Bus Threshold	16	U
3	Supp. Bus Alarm	16	U

8.2.3.7 Settings

Addr.	Parameter	Setting Domain	Default Setting
1	Supp. Bus Voltage	0 V to 10 V	0 V
2	Supp. Bus Threshold	0.950 to 1.050	1.000
3	Supp. Bus Alarm	Supp. Bus Alarm	0

8.2.3.8 Information List

No.	Information	Data Size (Bits)	Type
1	Supp. Bus Voltage	16	U
2	Supp. Bus Threshold	16	U
3	Supp. Bus Alarm	16	U

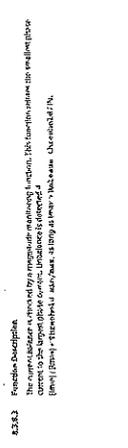


8.2.3.9 Overview of Function

The Current-Balance Supervision (CBS) monitors the balance of the secondary current sensors to ensure the balance is correct. This enables the CBS to detect a current balance error and to generate a current balance error alarm.

8.2.3.10 Structure of the Function

The Current-Balance Supervision function is located in the Power System data of each 1-phase current sensor.



8.2.3.11 Structure of the Function

The Current-Balance Supervision function is located in the Power System data of each 1-phase current sensor.

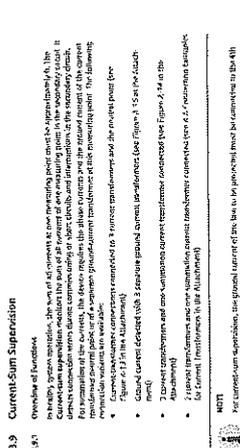


Figure 8.2.4 - Structure of the Current-Balance Supervision

8.2.3.12 Settings

Addr.	Parameter	Setting Domain	Default Setting
1	Supp. Bus Voltage	0 V to 10 V	0 V
2	Supp. Bus Threshold	0.950 to 1.050	1.000
3	Supp. Bus Alarm	Supp. Bus Alarm	0

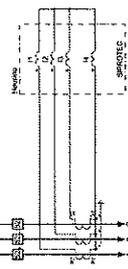
8.2.3.13 Information List

No.	Information	Data Size (Bits)	Type
1	Supp. Bus Voltage	16	U
2	Supp. Bus Threshold	16	U
3	Supp. Bus Alarm	16	U

Device Operating Hours
 The $2\pi \cdot 60 \cdot \text{THRESHOLD}$ value inside the register is used to calculate the number of operating hours of the device. The value of the register is updated when the device is powered on.

8.4.2 Analog Channel Supervision via Fast Current-Sum

- 8.4.2.1 Overview of Function**
- The device can monitor multiple digital channels by performing the following steps:
 - Configuration of the correct parameters of the device (analog-digital converter, based on the user requirements).
 - Configuration of the correct parameters of the device (analog-digital converter, based on the user requirements).
 - Configuration of the correct parameters of the device (analog-digital converter, based on the user requirements).
 - Configuration of the correct parameters of the device (analog-digital converter, based on the user requirements).
 - Configuration of the correct parameters of the device (analog-digital converter, based on the user requirements).



NOTE
 The analog channel configuration is not available when the AN converter is in a high-impedance state. The ADC is always in a high-impedance state when the device is in a high-impedance state. The device is always in a high-impedance state when the device is in a high-impedance state.

8.4.2.2 Structure of the Function

The structure of the function is shown in the block diagram. The function is implemented in the device's internal logic. The function is implemented in the device's internal logic.

NOTE
 The structure of the function is shown in the block diagram. The function is implemented in the device's internal logic. The function is implemented in the device's internal logic.

Function Description
 Shows the correct values are reported.
 The slope of the characteristic is 1 (1 bit per point).
 The slope of the characteristic is 1 (1 bit per point).
 The slope of the characteristic is 1 (1 bit per point).

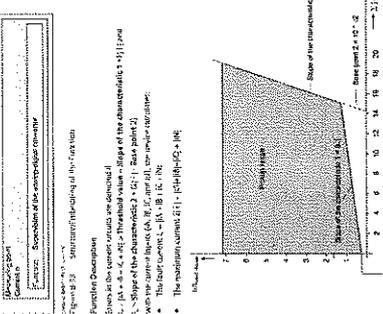


Figure 8.34 Characteristic Curve of the Supervision of the Device Internal Analog Digital Converters

NOTE
 The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point).

Function Description
 Shows the correct values are reported.
 The slope of the characteristic is 1 (1 bit per point).
 The slope of the characteristic is 1 (1 bit per point).
 The slope of the characteristic is 1 (1 bit per point).

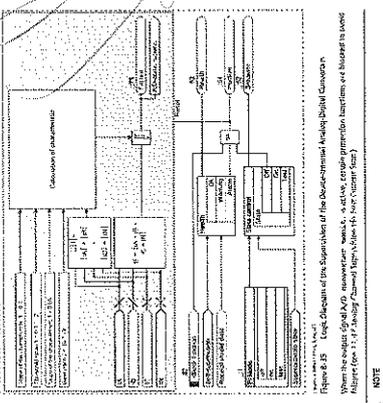


Figure 8.35 Logic Diagram of the Supervision of the Device Internal Analog Digital Converters

NOTE
 The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point).

NOTE
 The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point).

NOTE
 The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point). The slope of the characteristic is 1 (1 bit per point).

Supervision Limits

The slope of characteristic 2 is 1 (1 bit per point). The slope of characteristic 2 is 1 (1 bit per point). The slope of characteristic 2 is 1 (1 bit per point).

8.5 Supervision of Device Firmwares

- The device firmware (except firmware) is stored in the device's internal memory. The device firmware (except firmware) is stored in the device's internal memory.
- The device firmware (except firmware) is stored in the device's internal memory. The device firmware (except firmware) is stored in the device's internal memory.
- The device firmware (except firmware) is stored in the device's internal memory. The device firmware (except firmware) is stored in the device's internal memory.
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The device firmware (except firmware) is stored in the device's internal memory. The device firmware (except firmware) is stored in the device's internal memory.

Supervision Limits

The slope of characteristic 2 is 1 (1 bit per point). The slope of characteristic 2 is 1 (1 bit per point). The slope of characteristic 2 is 1 (1 bit per point).

8.6 Supervision of Hardware Configuration

- The device hardware (except firmware) is stored in the device's internal memory. The device hardware (except firmware) is stored in the device's internal memory.
- The device hardware (except firmware) is stored in the device's internal memory. The device hardware (except firmware) is stored in the device's internal memory.
- The device hardware (except firmware) is stored in the device's internal memory. The device hardware (except firmware) is stored in the device's internal memory.
- The device hardware (except firmware) is stored in the device's internal memory. The device hardware (except firmware) is stored in the device's internal memory.
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The device hardware (except firmware) is stored in the device's internal memory. The device hardware (except firmware) is stored in the device's internal memory.

359

9.2 Structure of the Function

Depending on the implementation of the function group, there may be one or more sub-groups. The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
	Operational measured values	...
Fundamental components	Fundamental components	...
	Fundamental components	...
Symmetrical components	Symmetrical components	...
	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...
	Phasor measurement unit (PMU)	...

The following table shows the relationship between the function group and the sub-groups. The relationship is defined by the following table:

Function Group	Sub-Group	Relationship
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

9.3 Operational Measured Values

Operational measured values are measured in different function groups. The values can be of different primary and secondary values and are measured in different function groups.

$$\begin{aligned}
 & \text{Active power } P = \sum_{k=1}^n U_k I_k \cos \varphi_k \\
 & \text{Reactive power } Q = \sum_{k=1}^n U_k I_k \sin \varphi_k \\
 & \text{Complex power } S = \sum_{k=1}^n U_k I_k \\
 & \text{Active power factor } \cos \varphi = \frac{P}{S} \\
 & \text{Reactive power factor } \sin \varphi = \frac{Q}{S}
 \end{aligned}$$

The following table shows the relationship between the function group and the sub-groups. The relationship is defined by the following table:

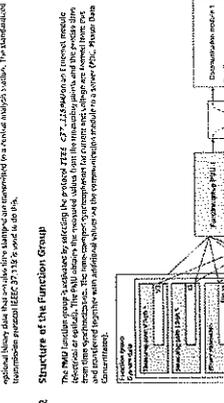
Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

9.5 Phasor Measurement Unit (PMU)

A Phasor Measurement Unit (PMU) measures the phasor values of current and voltage. These values are used for the calculation of the active and reactive power. The structure of the PMU is defined by the following table:



The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

9.4 Fundamental and Symmetrical Components

The fundamental and symmetrical components are calculated from the measured values through a Fourier filter. The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...

9.5 Structure of the Function Group

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

The structure of the function group is defined by the following table:

Function Group	Sub-Group	Structure
Operational measured values	Operational measured values	...
Fundamental components	Fundamental components	...
Symmetrical components	Symmetrical components	...
Phasor measurement unit (PMU)	Phasor measurement unit (PMU)	...

